GHP-SERIES

PD-97871C

Hybrid – High Reliability
Radiation Hardened DC-DC Converter
120V Input, Single and Dual Output

Features

- Total Dose > 100 kRads(Si)
- SEE Hardened to LET up to 82 MeV·cm^2/mg
- Low Weight < 110 grams
- Low Input and Output Noise
- Magnetically Coupled Feedback
- 95V to 140V DC Input Range
- Up to 120W Output Power
- Single and Dual Output Models
  Include 3.3, 5, 12, 15, ±5, ±12 and ±15V
- High Efficiency - to 86%
- -55°C to +125°C Operating Temperature Range
- 100MΩ @ 500VDC Isolation
- Under-Voltage Lockout
- Short Circuit and Overload Protection
- Adjustable Output with an External Resistor
- Remote Sense on Single Output Models
- Synchronization Input and Output
- External Inhibit
- 3.3 Million hour MTBF

Product Summary

- **Part number**: GHP12003R3S, GHP12005S, GHP12012S, GHP12015S, GHP12005D, GHP12012D, GHP12015D

GHP

Potential Applications

- Geostationary Earth Orbit Satellites (GEO)
- Deep Space Satellites / Probes

Product Validation

Validated according to MIL-PRF- 38534 for high-reliability applications

Ordering Information

Orderable Part Numbers

Part Number	Package	Screening Level
GHP12003R3S/CKA	Solder Dipped	Class K
GHP12003R3S/CKC	Gold Plated	Class K
GHP12005S/CKA	Solder Dipped	Class K
GHP12005S/CKC	Gold Plated	Class K
GHP12012S/CKA	Solder Dipped	Class K
GHP12012S/CKC	Gold Plated	Class K
GHP12015S/CKA	Solder Dipped	Class K
GHP12015S/CKC	Gold Plated	Class K
GHP12005D/CKA	Solder Dipped	Class K
GHP12005D/CKC	Gold Plated	Class K
GHP12012D/CKA	Solder Dipped	Class K
GHP12012D/CKC	Gold Plated	Class K
GHP12015D/CKA	Solder Dipped	Class K
GHP12015D/CKC	Gold Plated	Class K

Description

The GHP-Series of DC-DC converters are radiation hardened, high reliability converters specifically designed in response to the need for moderate power, high efficiency and well-regulated output required by the modern-day space design applications. Their small size and low weight make them ideal for applications such as geostationary earth orbit satellites and deep space probes. They exhibit a high tolerance to total ionizing dose, single event effects and environmental stresses such as temperature extremes, mechanical shock, and vibration.

The converters incorporate a fixed frequency single ended forward topology with magnetic feedback and an internal EMI filter that utilizes multilayer ceramic capacitors that are subjected to extensive lot screening for optimum reliability. By using two stage filtering these converters produce low input and output noise. External inhibit and synchronization input and output allow these converters to be easily incorporated into larger power systems. They are enclosed in a hermetic 3" x 2" x 0.475" package constructed of an Aluminum-Silicon-Carbide (AlSiC) base and an Alloy 48 ring frame and they weigh less than 110 grams. The package utilizes rugged ceramic feed-through copper core pins and is sealed using parallel seam welding.

Manufactured in a facility fully qualified to MIL-PRF-38534, these converters are fabricated utilizing DLA Land and Maritime qualified processes. For available screening options, refer to device screening table in the data sheet

Non-flight versions of the GHP-Series converters are available for system development purposes. Variations in electrical specifications and screening to meet custom requirements can be accommodated

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1 Specification and Electrical Performance

1.1 Maximum and Operating Table

Table 1 Absolute Maximum Rating and Recommended Operating Conditions

Absolute Maximum Rating		**Recommended Operating Conditions**	
Input voltage	-0.5V_{DC} to +160V_{DC}	Input voltage [1]	+95V_{DC} to +140V_{DC}
Output power	Internally limited	Output power	0 to Max. Rated
Lead Temperature	+300°C for 10 seconds	Operating Temperature	-55°C to +85°C
Operating Temperature	-55°C to +125°C	Operating Temperature [1]	-55°C to +70°C
Storage Temperature	-55°C to +125°C		

1.2 Electrical Performance Characteristics

Table 2 Electrical Characteristics

Parameter	**Group A Subgroups**	**Conditions** -55°C ≤ TC ≤ +85°C V_{IN} = 120V DC ± 5%, CL = 0 unless otherwise specified	**Limits** Min	Nom	Max	**Unit**
Input voltage (V_{IN})			95	120	140	V
Output Voltage (V_{OUT})						
GHP12003R3S	1		3.28	3.30	3.32	
GHP12005S	1		4.98	5.00	5.02	
GHP12012S	1		11.95	12.0	12.05	
GHP12015S	1		14.94	15.0	15.06	
GHP12005D	1		±4.95	±5.00	±5.05	
GHP12012D	1		±11.95	±12.0	±12.05	
GHP12015D	1	I_{OUT} = 100% rated load Note 4	±14.94	±15.0	±15.06	V
GHP12003R3S	2,3		3.24		3.36	
GHP12005S	2,3		4.93		5.07	
GHP12012S	2,3		11.84		12.16	
GHP12015S	2,3		14.80		15.20	
GHP12005D	2.3		±4.90		±5.10	
GHP12012D	2,3		±11.84		±12.16	
GHP12015D	2,3		±14.80		±15.20	

For Notes to Electrical Performance Characteristics, refer to page 9

[1] Meets de-rating per MIL-STD-975

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Parameter	Group A Subgroups	Conditions -55°C ≤ TC ≤ +85°C V_{IN} = 120V DC ± 5%, CL = 0 unless otherwise specified	Limits Min	Nom	Max	Unit
Output power (P_{OUT}) GHP12003R3S GHP12005S GHP12012S GHP12015S GHP12005D GHP12012D GHP12015D	1,2,3	V_{IN} = 95, 120, 140 Volts, Note 2	 0 0 0 0 0 0 0		 66 100 120 120 100 100 100	W
Output current (I_{OUT}) GHP12003R3S GHP12005S GHP12012S GHP12015S GHP12005D GHP12012D GHP12015D	1,2,3	V_{IN} = 95, 120, 140 Volts, Note 2 Either Output, Note 3 Either Output, Note 3 Either Output, Note 3	 0 0 0 0 3.2 1.33 1.07		 20 20 10 8.0 16 6.67 5.33	A
Line regulation (VR_{LINE})	1,2,3	V_{IN} = 95, 120, 140 Volts I_{OUT} = 0, 50%, 100% rated, Note 4	-10		-10	mV
Load regulation (VR_{LOAD})	1,2,3	I_{OUT} = 0, 50%, 100% rated, Note 4 V_{IN} = 95, 120, 140 Volts	-0.5		0.5	%
Cross regulation (VR_{CROSS}) GHP12005D GHP12012D GHP12015D	1,2,3	Duals only, Note 5 V_{IN} = 95, 120, 140 Volts	 -5.0 -3.0 -3.0		 5.0 3.0 3.0	%
Total regulation (Line, Load, and Temperature)	1,2,3	V_{IN} = 95, 120, 140 Volts I_{OUT} = 0, 50%, 100% rated, Dual Model is measured From +Output to –Output, Note 13	-2.0		2.0	%
Input current (I_{IN})	1,2,3	I_{OUT} = 0, Pin 3 open, Note 14 Pin 3 shorted to Pin 2		70 2.5	100 5.0	mA

For Notes to Electrical Performance Characteristics, refer to page 9

Parameter	Group A Subgroups	Conditions -55°C ≤ TC ≤ +85°C V_{IN} = 120V DC ± 5%, CL = 0 unless otherwise specified	Limits Min	Nom	Max	Unit
Output ripple (V_{RIP})						
GHP12003R3S	1,2,3	V_{IN} = 95, 120, 140 Volts I_{OUT} = 100% rated load Notes 4, 6		10	50	mV p-p
GHP12005S				15	50	
GHP12012S				25	60	
GHP12015S				25	60	
GHP12005D				20	60	
GHP12012D				20	60	
GHP12015D				20	60	
Input ripple current	1,2,3	I_{OUT} = 100% rated load, Note 1			15	mArms
Switching frequency (F_S)	1,2,3	Sync. Input (Pin 4) open	450	500	550	kHz
Efficiency (E_{FF})						
GHP12003R3S	1,2,3	I_{OUT} = 100% rated load Note 4	68	73		%
GHP12005S			78	82		
GHP12012S			81	85		
GHP12015S			82	86		
GHP12005D			78	82		
GHP12012D			81	84		
GHP12015D			82	86		
Inhibit Input						
Converter Off	1,2,3	Logic Low on Pin 3	-0.5		0.7	V
Sink current		Note 1			100	µA
Converter On		Logic High on Pin 3, Note 3	2.4		50	V
Sink current		Note 1			100	µA
Synchronization Input						
frequency range		Ext. Clock on Sync. Input (Pin 4), Note 1	450		600	kHz
pulse high level			4.0		10	V
pulse low level			-0.5		0.5	V
pulse transition time			40			V/µs
pulse duty cycle			20		80	%
Current Limit Point Expressed as a percentage of full rated load current	1,2,3	V_{OUT} = 90% of Nominal, Note 4			145	%
Power dissipation, load fault (P_D)	1,2,3	Short Circuit, Overload, Note 8			35	W

For Notes to Electrical Performance Characteristics, refer to page 9

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Specification and Electrical Performance

Parameter	Group A Subgroups	Conditions -55°C ≤ TC ≤ +85°C V_{IN} = 120V DC ± 5%, CL = 0 unless otherwise specified	Limits Min	Nom	Max	Unit
Output response to step load changes (V_{TLD})	4,5,6	Half Load to/from Full Load, Notes 4,9				mVpk
GHP12003R3S			-170		170	
GHP12005S			-450		450	
GHP12012S			-600		600	
GHP12015S			-750		750	
GHP12005D			-450		450	
GHP12012D			-750		750	
GHP12015D			-750		750	
Recovery time, step load changes (T_{TLD})	4,5,6	Half Load to/from Full Load, Notes 4,9,10			200	µs
Output response to step line changes (V_{TLN})		95V to/from 140V I_{OUT} = 100% rated load, Notes 1,4,11	-150		150	mVpk
Recovery time, step line changes (T_{TLN})		95V to/from 140V I_{OUT} = 100% rated load, Notes 1,4,10,11			200	µs
Turn-on Response	4,5,6	No Load, Full Load Notes 4,12				
Overshoot (V_{OS})					2.0	%
Turn-on Delay (T_{DLY})			0.5		8.0	ms
Capacitive Load (C_L)		I_{OUT} = 100% rated load No effect on DC performance Notes 1,4,7 Each output on duals				µF
GHP12003R3S					6000	
GHP12005S					5000	
GHP12012S					1000	
GHP12015S					1000	
GHP12005D					1000	
GHP12012D					500	
GHP12015D					500	
Line Rejection		I_{OUT} = 100% rated load DC to 50kHz, Notes 1, 4	40			dB
Isolation	1	Input to Output or Any Pin to Case except Pin 6, test @ 500VDC	100			MΩ
Device Weight					110	g
MTBF		MIL-HDBK-217F2, SF, 35°C	3.3×10^6			Hrs

For Notes to Electrical Performance Characteristics, refer to page 9

Notes: Electrical Performance Characteristics Table

1. Parameter is tested as part of design characterization or after design changes. Thereafter, parameter shall be guaranteed to the limits specified.
2. Parameter verified during line and load regulation tests.
3. Output load current must be distributed such that at least 20% of the total load current is being provided by one of the outputs.
4. Load current split equally between outputs on dual output models.
5. Cross regulation is measured with 20% rated load on output under test while changing the load on the other output from 20% to 80% of rated.
6. Guaranteed for a D.C. to 20MHz bandwidth. Tested using a 20kHz to 10MHz bandwidth.
7. Capacitive load may be any value from 0 to the maximum limit without compromising dc performance. A capacitive load in excess of the maximum limit may interfere with the proper operation of the converter's overload protection, causing erratic behavior during turn-on.
8. Overload power dissipation is defined as the device power dissipation with the load set such that V_{OUT} = 90% of nominal.
9. Load step transition time ≥10 µs.
10. Recovery time is measured from the initiation of the transient to where V_{OUT} has returned to within ± 1% of its steady state value.
11. Line step transition time ≥100 µs.
12. Turn-on delay time from either a step application of input power or a logic low to a logic high transition on the inhibit pin (pin 3) to the point where V_{OUT} = 90% of nominal.
13. Total regulation at EOL is +/- 3% maximum.
14. The input current is minimized with an output load of 300mW to 400mW. If a system design requires converter operation at or near zero load (e.g. a system standby mode), then it is recommended that a resistive preload of 300mW to 400mW will be added to converter's output (s). The small preload will reduce the converter's "no load" input current from approximately 70mA to approximately 30mA.

2 Block Diagram

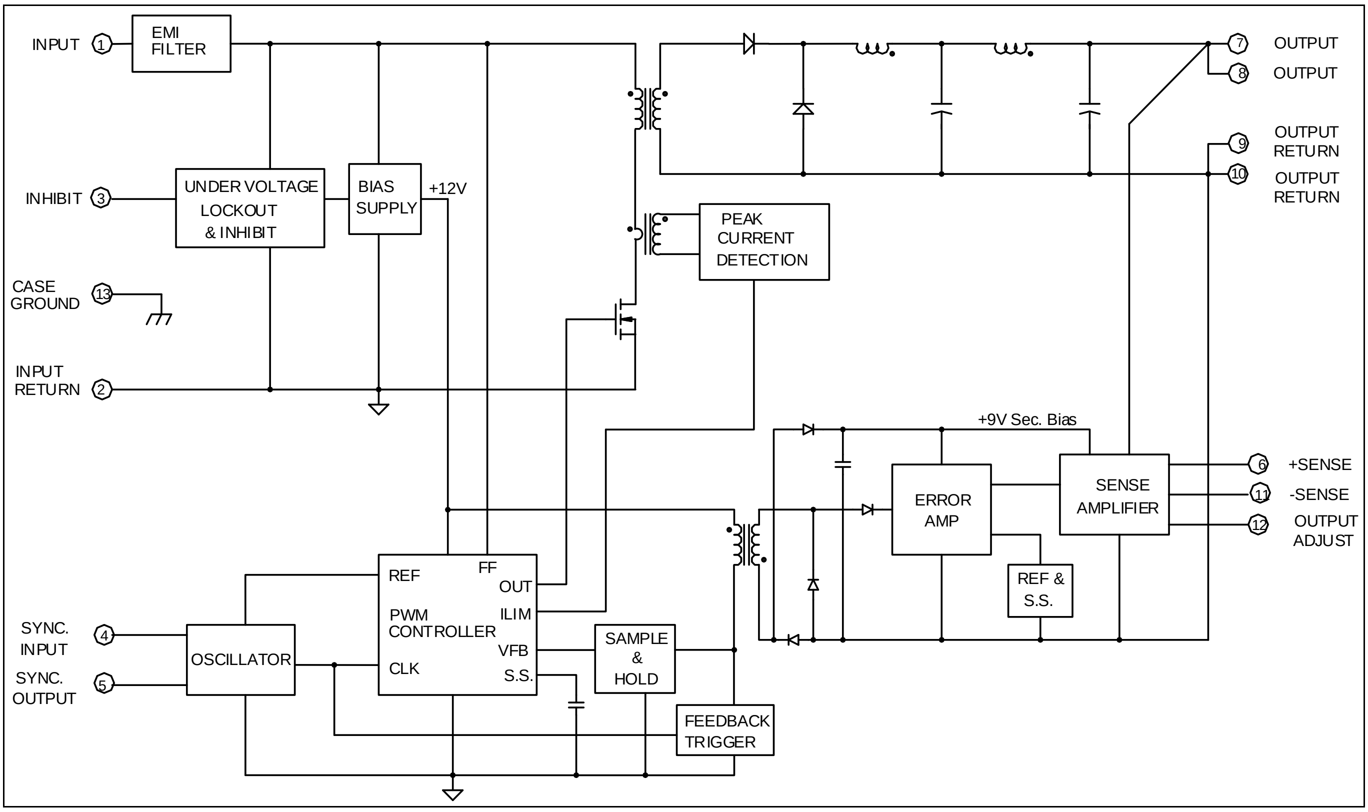


Figure 1 **Block Diagram - Single Output**

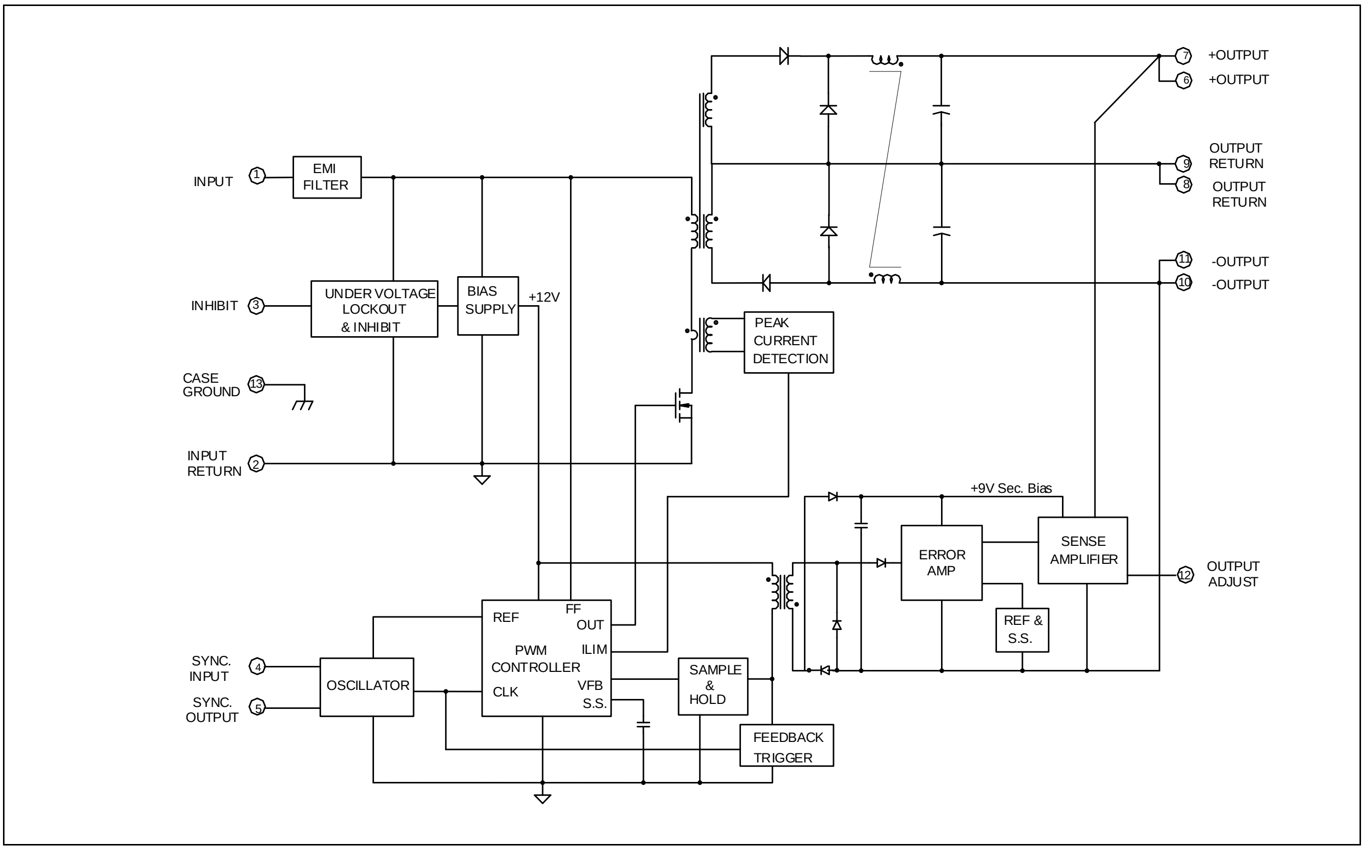


Figure 2 **Block Diagram - Dual Output**

3 Radiation Performance table and Application Notes

3.1 Radiation Performance table

Table 3 Radiation Performance Characteristics

Test	Conditions	Min	Typ	Unit
Total Ionizing Dose (Gamma)	MIL-STD-883, Method 1019 Operating bias applied during exposure, Full Rated Load, V_{IN} = 120V	100	150	kRads (Si)
Single Event Effects SEU, SEL, SEGR, SEB	Heavy ions (LET) Operating bias applied during exposure, Full Rated Load, V_{IN} = 95, 120, 140V **Test Lab: Texas A & M University**	82		MeV·cm²/mg

3.2 Application Notes

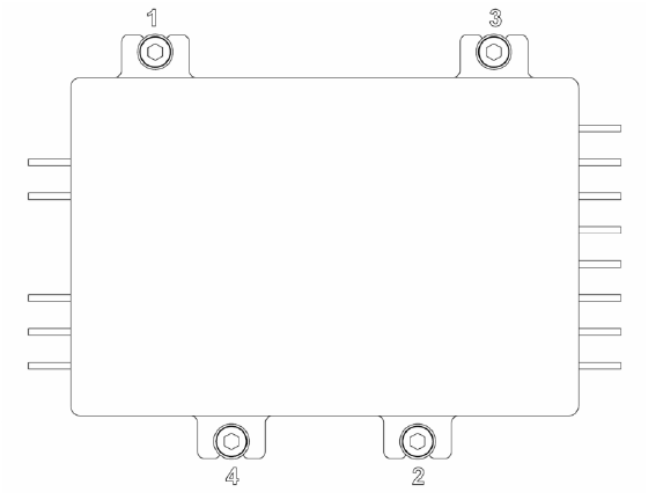
A) Attachment of the Converter:

The following procedure is recommended for mounting the converter for optimum cooling and to circumvent any potential damage to the converter.

Ensure that flatness of the plate where GHP converter to be mounted is no greater than 0.003" per linear inch. It is recommended that a thermally conductive gasket is used to promote the thermal transfer and to fill any voids existing between the two surfaces. IR HiRel recommends Sil-Pad 2000 with the thickness of 0.010". The shape of the gasket should match the footprint of the converter including the mounting flanges. The gasket is available from IR HiRel. The GHP-Series converter requires either M3 or 4-40 size screws of attachment purposes.

The procedure for mounting the converter is as follows:

1. Check the mounting surfaces and remove foreign material, burrs if any or anything that may interfere with the attachment of the converter.
2. Place the gasket on the surface reserved for the converter and line it up with the mounting holes.
3. Place the converter on the gasket and line both up with mounting holes.
4. Install screws using appropriate washers and tighten by hand (~ 4 in·oz) in the sequence shown below.



5. Tighten the screws with an appropriate torque driver. Torque the screws up to 6 in·lb in the sequence shown above

B) Output Voltage Adjustment

Single Output:

To adjust the output voltage of the single output models, a resistor (R_{ADJ}) is connected between the Adjust pin (Pin 12) and either the positive or negative remote sense pins, depending on whether the output voltage is to be adjusted higher or lower than the nominal set-point. This allows the outputs to be reliably adjusted by approximately +10% to -20% of the nominal output voltage. Refer to Fig. 3 and use equations provided to calculate the required resistance (RA_{DJ}).

Note: The output voltage adjust equation does not work as described for the 3.3V Single model. The adjust range for 3.3V model is limited to 3.252V to 3.460V.

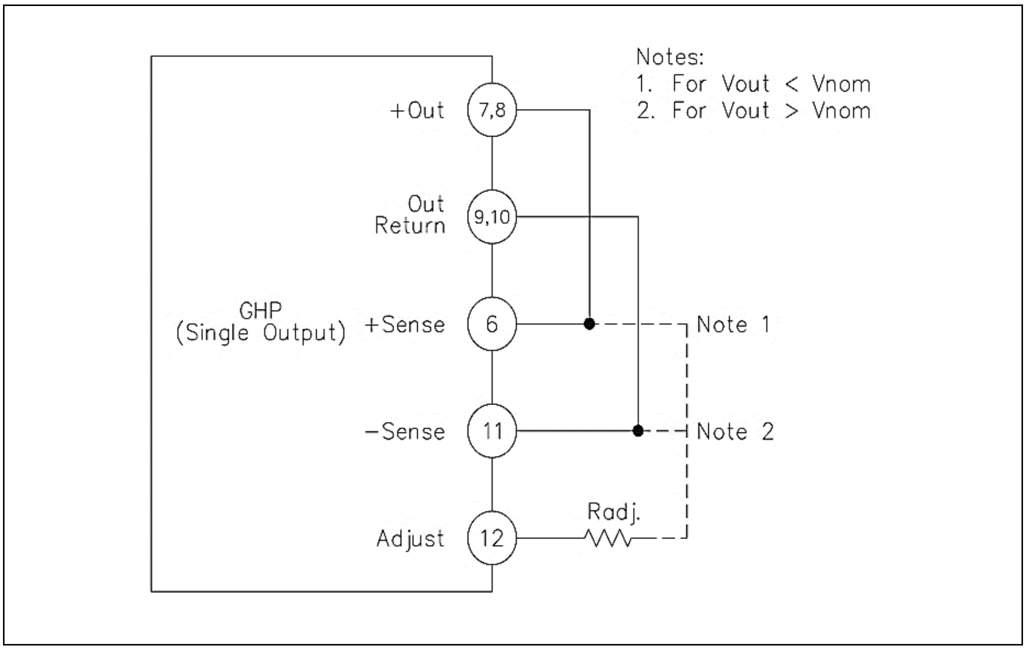


Figure 3 **Configuration for Adjusting Single Output Voltage**

For all Single Output Models, to adjust the output voltages higher:

$$R_{ADJ} = \frac{10 \times (V_{NOM} - 2.5)}{V_{OUT} - V_{NOM}} - 50$$

To adjust the : R_{ADJ} is in kOhms

R_{ADJ} is connected to the -Out pin and $V_{NOM} < V_{OUT} < 1.1V_{NOM}$ (Fig. 3, Note 2)
V_{NOM} is the nominal output voltage with the Adjust Pin left open
V_{OUT} is the desired output voltage

For **all Single Output Models,** to adjust the output voltages lower:

$$R_{ADJ} = \frac{4 \times (V_{NOM} - 2.5) \times (V_{OUT} - 2.5)}{V_{NOM} - V_{OUT}} - 50$$

To adjust the: R_{ADJ} is in kOhms

R_{ADJ} is connected to the +Out pin and $0.8V_{NOM} < V_{OUT} < V_{NOM}$ (Fig.3, Note 1)
V_{NOM} is the nominal output voltage with the Adjust Pin left open
V_{OUT} is the desired output voltage

Dual Output:

To adjust the output voltage of the dual output models, a resistor (R_{ADJ}) is connected between the Adjust pin (Pin 8) and either output. This allows the outputs to be reliably adjusted by approximately +10% to -20% of the nominal output voltage. Refer to Fig. 4 and use equations provided to calculate the required resistance (R_{ADJ}).

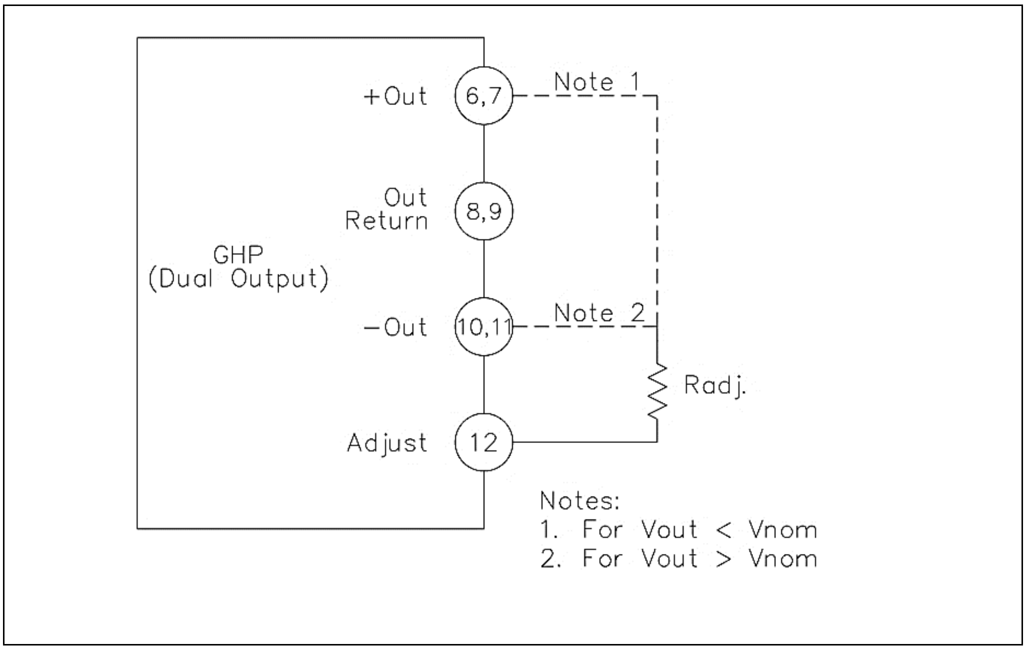


Figure 4 Configuration for Adjusting Dual Output Voltage

For all **Dual Output Models**, to adjust the output voltages higher:

$$R_{ADJ} = \frac{10 \times (V_{NOM} - 1.25)}{V_{OUT} - V_{NOM}} - 75$$

To adjust the: R_{ADJ} is in kOhms

R_{ADJ} is connected to the -Out pin and $V_{NOM} < V_{OUT} < 1.1V_{NOM}$ (Fig. 4, Note 2)

V_{NOM} is the nominal magnitude of the output voltages with the Adjust pin left open

V_{OUT} is the desired magnitude of the output voltages

For all **Dual Output Models**, to adjust the output voltages lower:

$$R_{ADJ} = \frac{8 \times (V_{NOM} - 1.25) \times (V_{OUT} - 1.25)}{V_{NOM} - V_{OUT}} - 75$$

To adjust the: R_{ADJ} is in kOhms

R_{ADJ} is connected to the +Out pin and $0.8V_{NOM} < V_{OUT} < V_{NOM}$ (Fig. 4, Note 1)

V_{NOM} is the nominal magnitude of the output voltages with the Adjust pin left open

V_{OUT} is the desired magnitude of the output voltages

4 Mechanical Outlines

Note: For the most updated package outline, please see the website: [GHP-SERIES]

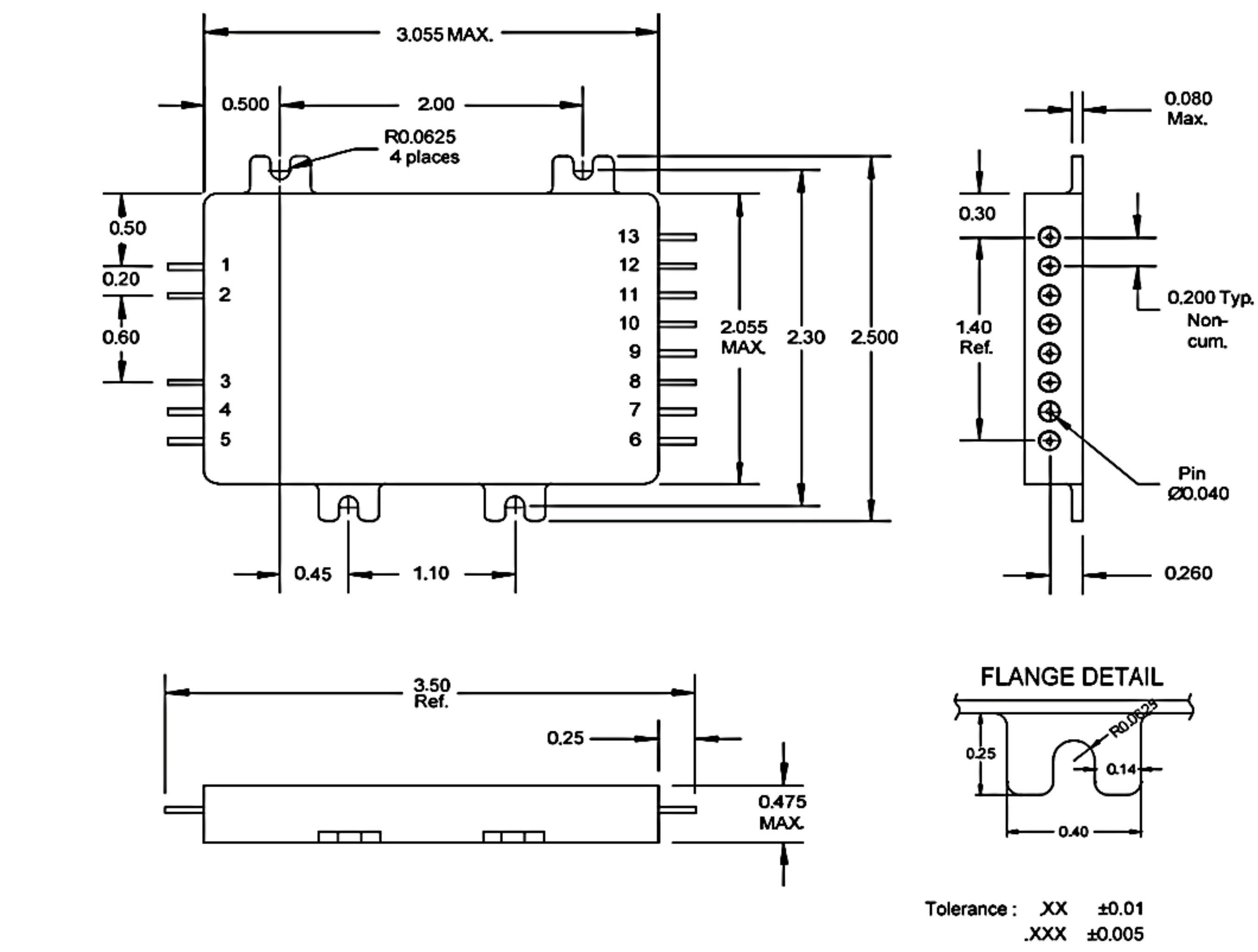


Figure 5 Package outline

5 Pin Designation

5.1 Pin Designation (Single / Dual)

Table 4 Designation

Single Output		Dual Output	
Pin Number	**Designation**	**Pin Number**	**Designation**
1	Input	1	Input
2	Input Return	2	Input Return
3	Inhibit	3	Inhibit
4	Sync. Input	4	Sync. Input
5	Sync. Output	5	Sync. Output
6	+Sense	6	+ Output
7	Output	7	+ Output
8	Output	8	Output Return
9	Output Return	9	Output Return
10	Output Return	10	-Output
11	-Sense	11	-Output
12	Output Adjust	12	Output Adjust
13	Case Ground	13	Case Ground

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6 Devices Screening

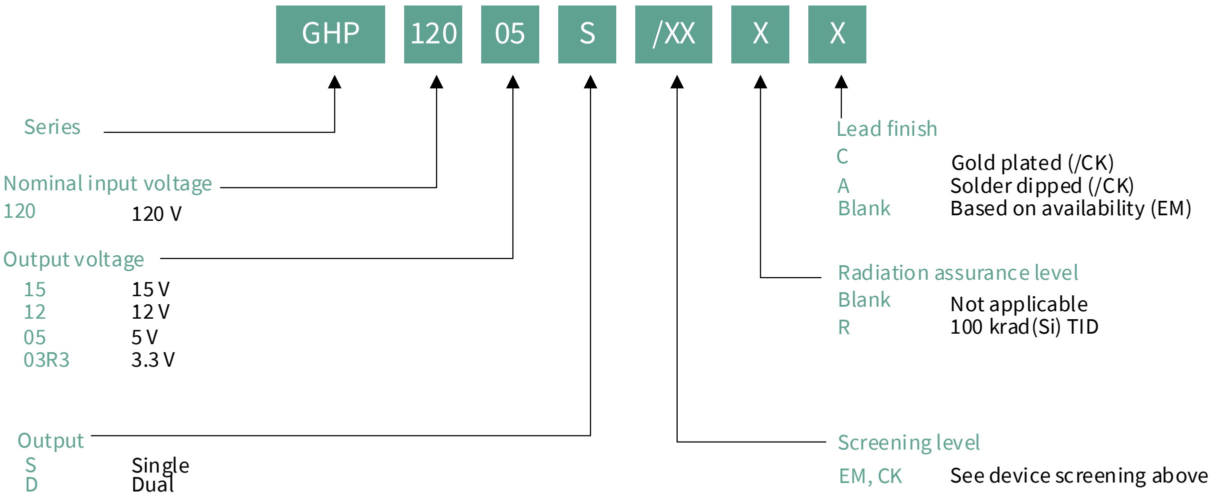
Part Number Designator		/EM①	/CK②
Compliance Level	**MIL-PRF-38534**	—	**K level compliant**
Certification Mark		—	**CK**
Screening Requirement	**MIL-STD-883 Method**	—	—
Temperature Range	—	Room Temperature	-55°C to +85°C
Element Evaluation	MIL-PRF-38534	N/A	Class K
Non-Destructive Bond Pull	2023	N/A	Yes
Internal Visual	2017	IR Defined	Yes
Temperature Cycle	1010	N/A	Cond C
Constant Acceleration	2001, Y1 Axis	N/A	3000 Gs
PIND	2020	N/A	Cond A
Burn-In	1015	N/A	320 hrs @ 125°C (2 x 160 hrs)
Final Electrical (Group A)	MIL-PRF-38534 & Specification	Room Temperature	-55°C, +25°C, +85°C
PDA	MIL-PRF-38534	N/A	2%
Seal, Fine and Gross	1014	N/A	Cond CH
Radiographic	2012	N/A	Yes
External Visual	2009	IR Defined	Yes

Notes:

① **"EM" grade** parts are strictly intended to permit the customer to determine the electrical functionality of the device in the customer's application in ambient conditions. The use of EM devices in production applications presents an unquantifiable risk of failure and IR HiRel disclaims all responsibility for such failure.

② **"CK" grade** is the flight model (FM) compliant to K Level screening as defined in the DLA Land and Maritime MIL-PRF-38534 requirements, but is not necessarily a DLA Land and Maritime qualified SMD per MIL-PRF-38534. The governing document for this part number designator is the IR HiRel datasheet (this document). Radiation rating as stated in the "Radiation Performance Characteristics" section, is verified by analysis and test per IR HiRel internal procedure. The part is marked with the IR base part number and the "CK" certification mark.

Part Numbering



Revision history

Document version	Date of release	Description of changes
	05/19/2017	Datasheet (PD-97871)
Rev A	02/11/2020	Updated based on ECO-1110_29655
Rev B	04/30/2020	Updated based on ECO-1110_29731
Rev C	05/09/2022	Updated based on ECO-1110_30760

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An Infineon Technologies company

El Segundo, California 90245 USA

Do you have a question about this document?

Email: erratum@infineon.com

Document reference