

30CLJQ100

PD-94085E

Schottky Rectifier High Efficiency Series Surface Mount (SMD-0.5) 100V, 30A

Features

- Hermetically sealed
- Center tap
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Surface mount
- Light weight
- ESD rating: Class 3B per MIL-STD-750, Method 1020

Potential Applications

- DC-DC converter
- Protection circuits

Product Validation

Fully qualified according to MIL-PRF-19500 for space applications

Description

The 1N6843CCU3 center tap Schottky rectifier has been expressly designed to meet the rigorous requirements of high reliability environments. It is packaged in the hermetic surface mount SMD-0.5 ceramic package. The device's forward voltage drop and reverse leakage current are optimized for the lowest power loss and the highest circuit efficiency for typical high frequency switching power supplies and resonant power converters. Full MIL-PRF- 19500 quality conformance testing is available on source control drawings to TX, TXV and S quality levels.

Ordering Information

Table 1 Ordering options

Part number	Package	Screening Level
30CLJQ100	SMD-0.5	COTS
30CLJQ100B	SMD-0.5 with lead attach and formed	COTS
30CLJQ100SCSA	SMD-0.5 with lead attach	S-level
JANTX1N6843CCU3	SMD-0.5	JANTX
JANTXV1N6843CCU3	SMD-0.5	JANTXV
JANS1N6843CCU3	SMD-0.5	JANS

Product Summary

- **Part number:** 30CLJQ100
- **DLA part number:** JANS1N6843CCU3, JANTX1N6843CCU3, JANTXV1N6843CCU3
REF: MIL-PRF-19500/681
- **I_{F(AV)}:** 30A
- **V_{RRM} (per leg):** 100V
- **V_F @ 30A_p, T_J = 125°C (per leg) :** 0.95V
- **I_{FSM} @ t_p = 8.3ms half-sine (per leg) :** 100A

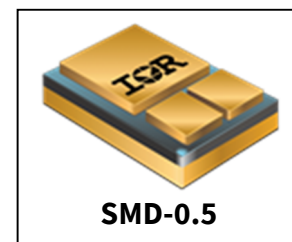


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Absolute Maximum Ratings

1 Absolute Maximum Ratings

Table 2 Absolute Maximum Ratings

Symbol	Parameter	Value	Unit
V_R	DC reverse voltage (per leg)	100	V
V_{RWM}	Working peak reverse voltage (per leg)	100	V
$I_{F(AV)}$	Max. average forward current (per package) ¹ - Refer to Fig. 5	30	A
I_{FSM}	Max. peak one cycle non-repetitive surge current (per leg) ²	100	A
T_J T_{STG}	Operating Junction and Storage Temperature Range	-65 to 150	°C
	Weight	1.0 (Typical)	g

¹ 50% duty cycle @ $T_c = 83^\circ\text{C}$, square waveform

² $t_p = 8.3$ ms half-sine

Device Characteristics

2 Device Characteristics

2.1 Electrical Characteristics

Table 3 Electrical Characteristics

Symbol	Parameter	Max.	Unit	Test Conditions					
V_F	Forward Voltage Drop (Per Leg) See Fig. 1 ¹	0.95	V	@5.0A	$T_J = -55^\circ\text{C}$				
		1.18	V	@15A					
		1.43	V	@30A					
				0.77	V	@5.0A	$T_J = 25^\circ\text{C}$		
				1.03	V	@15A			
				1.27	V	@30A			
						0.65	V	@5.0A	$T_J = 125^\circ\text{C}$
						0.77	V	@15A	
						0.95	V	@30A	
I_R	Reverse Leakage Current (Per Leg) See Fig. 2 ¹	0.01	mA	$T_J = 25^\circ\text{C}$	$V_R = \text{rated } V_R$				
		1.19	mA	$T_J = 100^\circ\text{C}$					
		5.0	mA	$T_J = 125^\circ\text{C}$					
C_J	Junction Capacitance (Per Leg)	275	pF	$V_R = 5V_{DC}$ (1MHz, 25°C)					
L_S	Series Inductance (Per Leg)	4.8(Typical)	nH	Measured from center of cathode pad to center of anode pad					

2.2 Thermal-Mechanical Specifications

Table 4 Thermal-Mechanical Specifications

Symbol	Parameter	Max.	Unit	Test Conditions
$R_{\theta JC}$	Thermal Resistance, Junction to Case (Per Leg)	3.5	$^\circ\text{C}/\text{W}$	DC operation See Fig. 4
$R_{\theta JC}$	Thermal Resistance, Junction to Case (Per Package)	1.75	$^\circ\text{C}/\text{W}$	DC operation
	Die Size (Typical)	84 x 84	mils	

¹ Pulse Width < 300 μs , Duty Cycle < 2%

Electrical Characteristics Curves

3 Electrical Characteristics Curves

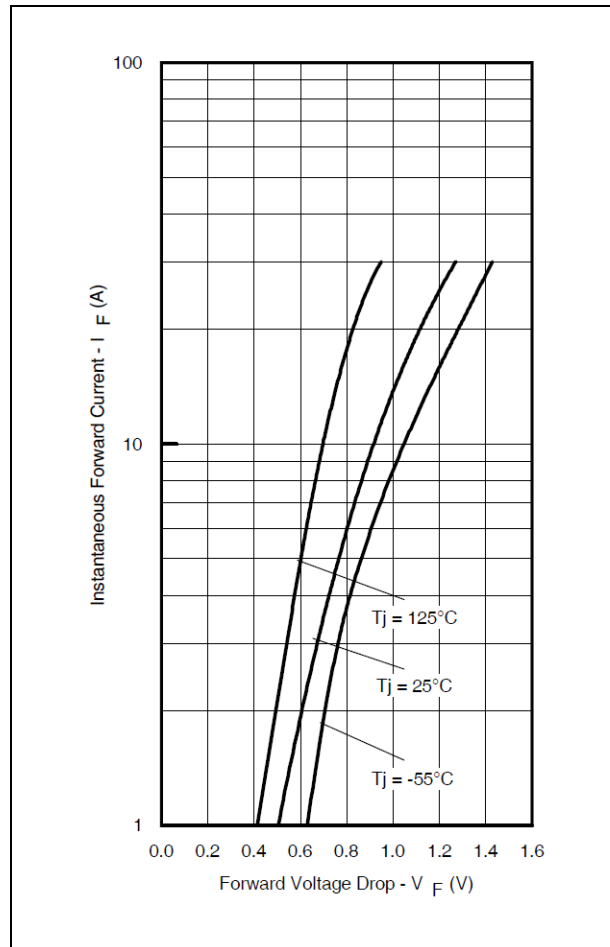


Figure 1 Maximum Forward Voltage Drop Characteristics (Per Leg)

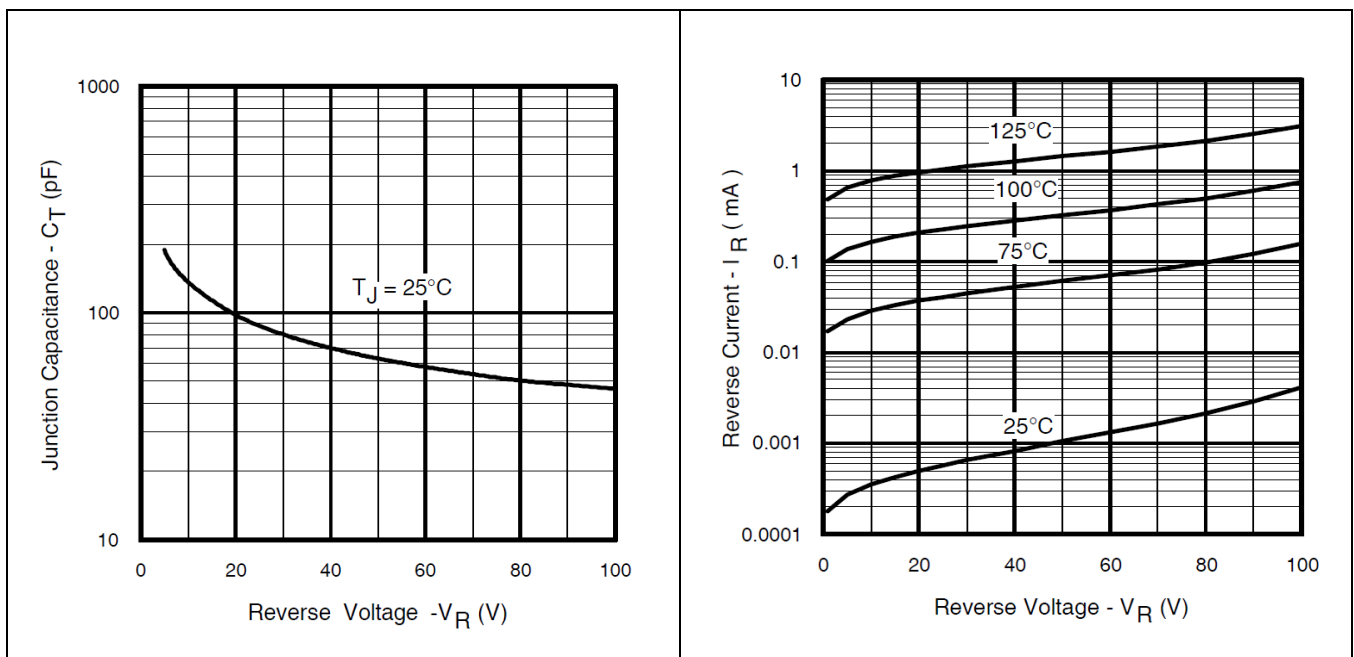


Figure 2 Typical Values of Reverse Current Vs. Reverse Voltage (Per Leg)

Figure 3 Typical Junction Capacitance Vs. Reverse Voltage (Per Leg)

Electrical Characteristics Curves

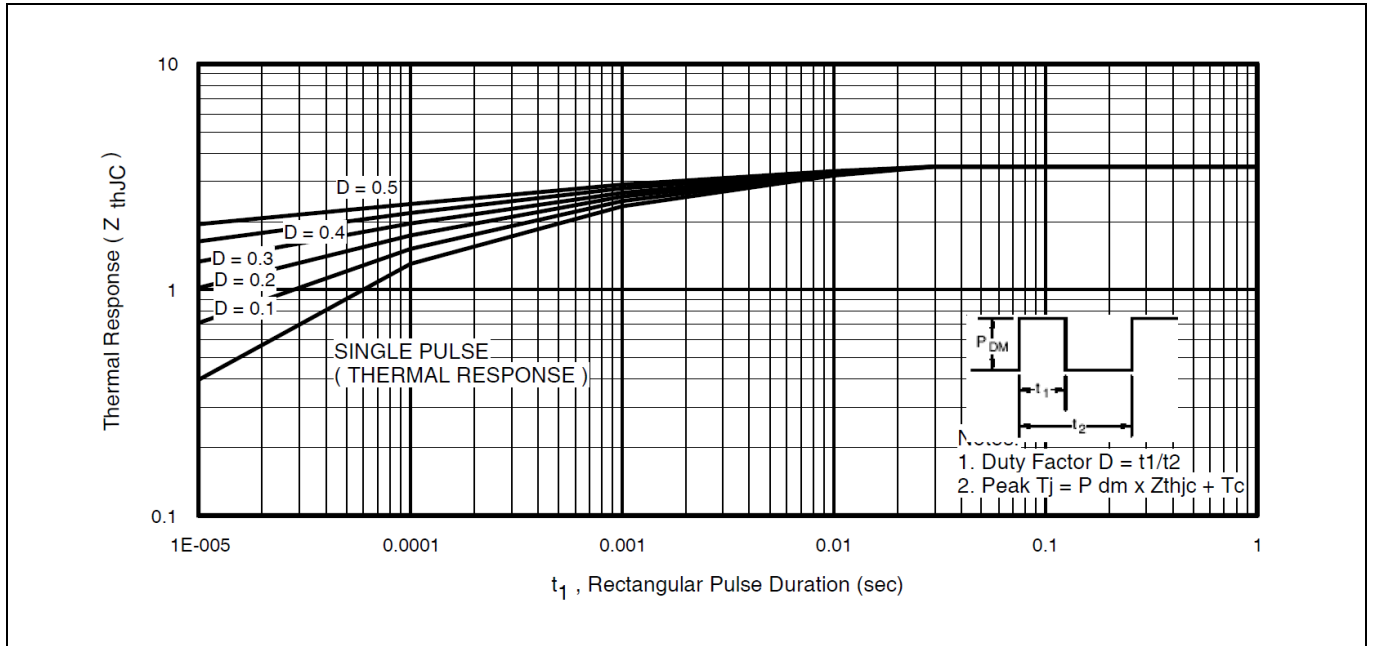


Figure 4 Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

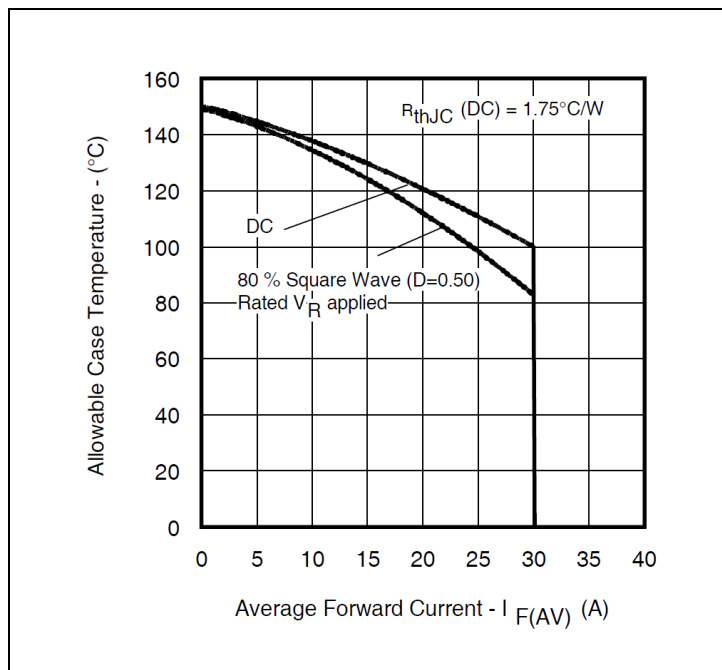


Figure 5 Maximum Allowable Case Temperature Vs. Average Forward Current (Per Package)

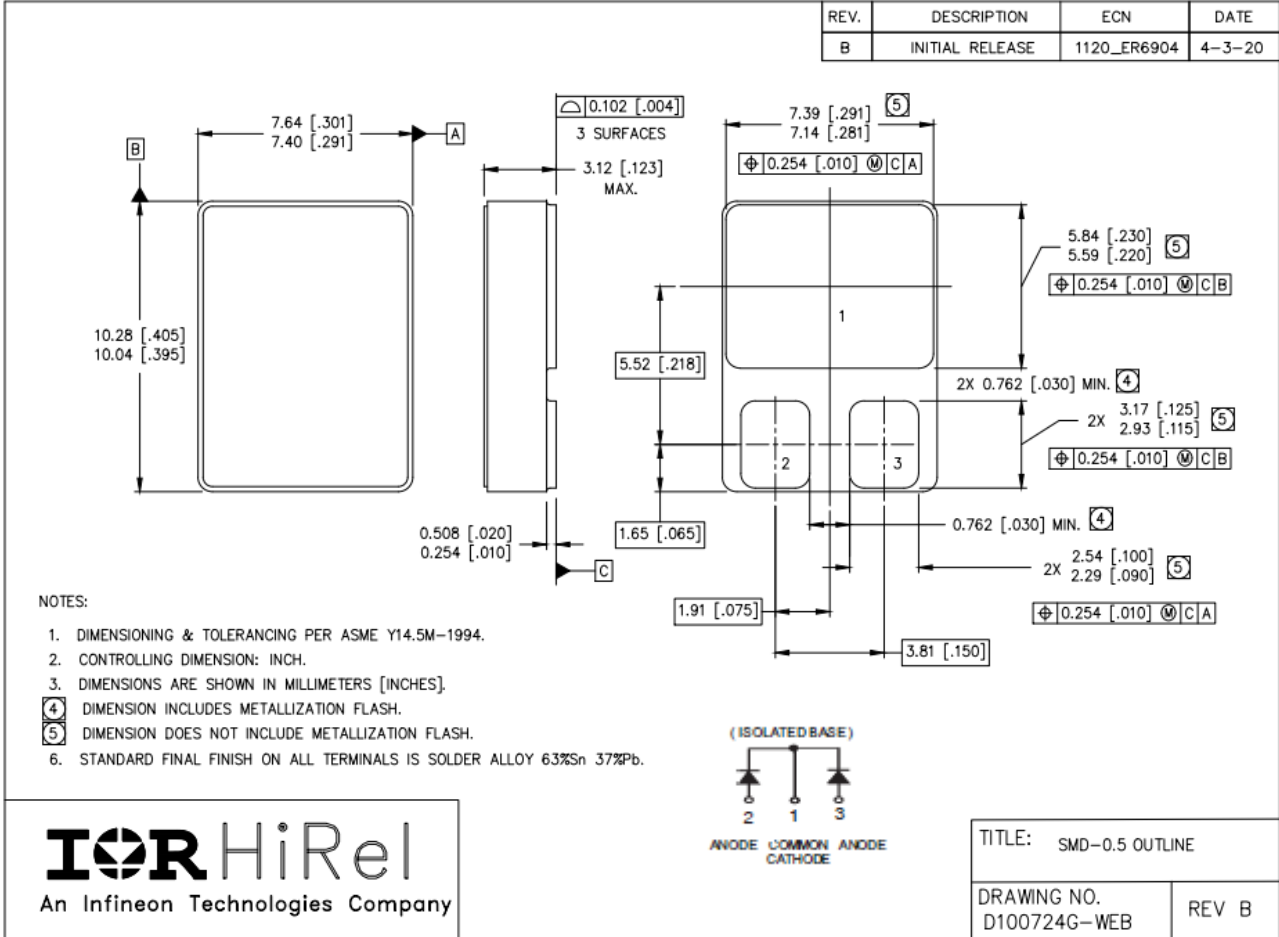
30CLJQ100

Schottky Rectifier High Efficiency Series

Package Outline

4 Package Outline

Note: For the most updated package outline, please see the website: [SMD-0.5](#)



Revision history

Revision history

Document version	Date of release	Description of changes
	01/23/2001	Final datasheet (PD-94085)
Rev A	08/10/2001	Updated Vf curve –page3
Rev B	11/29/2007	Updated fig 5 –page4
Rev C	05/13/2008	Updated per ECN-16060
Rev D	10/19/2012	Added ESD rating –page1
Rev E	06/15/2021	Updated per ECN-1120-08640

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Document reference

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