DC to AC Inverter IGBT Demo Board

Devices: IRGB4062DPBF (600V/24A Trench IGBT) High Side IGBTs
      IRG4BC20SD-PBF (600V/10A S-type Planar IGBT) Low Side IGBTs
      IRS2106S (600V half bridge driver IC)

The purpose of a DC to AC inverter is to convert DC voltage to a pure sinusoidal output voltage in applications such as UPS, solar inverter and frequency converter.

This demo board is designed to operate without fan up to 500W.

Figure 1. Connection diagram.

Equipment required:
200V, 3A DC power supply
20V, 100 mA DC power supply
120V/500W load bank such as resistors, light bulbs or a portable heater

Connection should be made according to the diagram shown on Figure 1.

Theory of operation:
A full bridge topology is used to implement the DC to AC inverter. During positive output half cycle, Q1 is sine pulse width modulated (sine PWM) while Q4 is kept on. During negative output half cycle, Q2 is sine pulse width modulated while Q3 is kept on. The switching frequency of the high side and low side IGBTs are 20 kHz and 60 Hz, respectively. This switching technique produces 60 Hz AC sine wave across the output capacitor C4 after inductor L1.

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Q1 and Q2 are chosen to be ultrafast trench IGBTs, IRGB4062DPBF which offers balanced conduction and switching losses at 20 kHz. Q3 and Q4 are standard type (S-type) planar IGBT since these IGBTs only switch at 60 Hz. Low conduction loss IGBT is essential for Q3 and Q4 since conduction loss is the dominant factor while switching loss is not at 60 Hz.

Each leg of the H-Bridge is driven using a high voltage gate driver IC, IRS2106SPBF, with bootstrap power supply technique for the high side. Using IRS2106SPBF eliminates the requirement for an isolated power supply for the high side drive. This translates into increase efficiency and parts count reduction of the overall system.

Some benefits of the H bridge topology and switching technique on this demo board are:
1. High efficiency since Q1 and Q2 copack diodes are not subjected to the freewheeling current and Q3 and Q4 have majority of conduction loss and very little switching loss.
2. No cross conduction possibility since switching is done on diagonal device pair only at any time (Q1 and Q4 or Q2 and Q3).
3. Operate from single DC bus supply eliminating the need for a negative DC bus.
4. IGBTs are driven using high voltage gate driver IC with bootstrap technique. There is no separate floating power supply required for the high side drive. Bootstrap capacitors for these drivers get refreshed every switching cycle (50 usec) when current freewheels on the low side IGBTs copackage diodes.

Figure 2. Schematic of the demo board
Operation:
Always start the system by applying power to the +20V power supply prior to applying +200V DC bus. With a 120V/500W resistive load connected, the output will be a nominal 120V/60 Hz AC sinusoidal voltage as shown in Figure 3. This figure also shows the output gate voltages across each IGBT showing 60 Hz commutation and 20 kHz sine PWM signals for low and high side devices, respectively.

WARNING
The output voltage is taken from the legs of the full bridge inverter after the L-C filter. In order to obtain the same sinusoidal output voltage waveform shown in Figure 3, an appropriate high voltage differential voltage probe is necessary. If such probe is not available, measure each output using a ground referenced oscilloscope probe and subtract the resulting waveforms on the oscilloscope. Please consult with individual oscilloscope manual on how to do this measurement.

Power Loss Comparisons:
Using the technique described in this demo board achieves the lowest power dissipation on a typical full bridge solar inverter application. Figure 4 summarizes differences in power dissipation based on different switching technique and IGBT combinations.
Reduction in Power Dissipation Using Combination of Trench and Standard Speed Planar IGBTs

Figure 4. Power Dissipation Comparisons of Different Switching Technique and IGBT Combinations.