




PROBLEM ADVISORY

1. TITLE (Class, Function, Type, etc.) Capacitor, Chip, Ceramic		2. DOCUMENT NUMBER FV5-P-06-01A	
		3. DATE (DD-MMM-YY) 19 MAY 2006	
4. MANUFACTURER AND ADDRESS Novacap Inc. 25136 Anza Dr. Valencia, CA 91355	5. PART NUMBER See Document	6. NATIONAL STOCK NUMBER Not Available	
	7. SPECIFICATION MIL-PRF-123	8. TYPE DESIGNATOR Not Available	
	9. LOT DATE CODE START See Document	10. LOT DATE CODE END See Document	
11. MANUFACTURER'S POINT OF CONTACT Brian Crowley	12. CAGE 65238	13. MANUFACTURER'S FAX (661) 295 5928	
14. MFR. POC PHONE (661) 295-5920	15. MANUFACTURER'S E-MAIL bcrowley@novacap.com		
16. CROSS REFERENCE VENDOR Not Available	17. CROSS REFERENCE CAGE Not Available	18. CROSS REFERENCE PART Not Available	
19. PROBLEM DESCRIPTION / DISCUSSION / EFFECT <p>THIS AMENDMENT PROVIDES NEW INFORMATION FROM INTERNATIONAL RECTIFIER CONCERNING ADDITIONAL PRODUCT PART NUMBERS IDENTIFIED FOR PURE TIN CONTENT - IN CONJUNCTION WITH GIDEP AAN-U-05-061. THOSE RESULTS ARE SHOWN IN SECTION 20 HEREIN. IN ADDITION, RESULTS FROM SUBSEQUENT TEST EVALUATION ON WHISKER GROWTH ARE ALSO PROVIDED HEREIN. PAGES 5 – 7 WERE ADDED AS PART OF THIS AMENDMENT.</p> <p>A DC/DC converter manufactured by International Rectifier was identified to contain a capacitor with a pure tin termination by EDS testing during a DPA. The identified capacitor was manufactured by Novacap.</p> <p>The capacitor was purchased in August 2001 to Novacap part number 1210X332K501NMH for which the termination designator "N" originally specified nickel barrier, 90Sn/10Pb (see attached copy from Novacap catalog). Novacap subsequently changed the "N" designator from nickel barrier, 90Sn/10Pb, to specify nickel barrier, 100% tin. The designator for 90sn/10Pb was changed to "Y". International Rectifier was not aware of the designator change. Further investigation has identified other parts purchased with the "N" designator that potentially have the same problem.</p> <p>MIL-PRF-38534, General Specification for Hybrid Microcircuits, paragraph E.4.2.7.1 states that the use of pure tin is prohibited from internal element finishes. Tin is considered to be pure if it contains less than three percent alloy material. In addition, NASA requirements prohibit the use of pure tin in any application due to a phenomenon called "whisker growth" and/or "sublimation" (refer to the following web site for further information. http://nepp.nasa.gov/whisker/). Industry studies have shown that the use of a pure tin finish can promote the growth of tin (Sn) whiskers, which might cause potential reliability risks related to electrical shorts, debris, or contamination. MIL-PRF-123, the Performance specification for fixed ceramic capacitors states that tin plating is prohibited as a final finish or as an undercoat. Tin-lead (Sn-Pb) finishes are acceptable provided that the minimum lead content is four percent. For additional information, see ASTM B545 (Standard Specification for Electrodeposited Coatings of Tin).</p> <p>To date, no field operational failures have been reported to International Rectifier that was directly or indirectly related to a 100% tin issue.</p>			
20. ACTION TAKEN/PLANNED IR has researched hybrids manufactured (capacitors, where used) with capacitors containing pure tin and a list of affected hybrid part numbers with date code is provided herein in Table I. Experiments were also conducted on representative hybrids to evaluate tin whisker growth on the capacitor terminations with pure tin. Results are provided herein.			
21. DATE MFR. NOTIFIED September 26, 2005	22. MANUFACTURER'S RESPONSE <input checked="" type="checkbox"/> REPLY ATTACHED <input type="checkbox"/> NO REPLY	23. ORIGINATOR ADDRESS/POINT OF CONTACT Paul Hebert, International Rectifier Hi-Rel (978) 514-6180 phebert@irf.com	
24. REPRESENTATIVE Paul Hebert, International Rectifier Hi-Rel	25. SIGNATURE 		26. DATE Oct. 18, 2005



25136 Anza Drive, Valencia CA 91355 • Tel (661) 295-5920 • Fax (661) 295-5928 • www.novacap.com

Brian Crowley
Quality Assurance Manager

October 17, 2005

Rich Perry
Quality Manager
International Rectifier Hi-Rel
2270 Martin Ave.
Santa Clara, CA 95050
Email: rperry@irf.com

Re: GIDEP FV5-P-06-01

Hello Rich,

We have completed our investigations regarding the problem as described in the GIDEP Advisory through records and data review. The findings and actions implemented since the manufacturing time period in question are referenced along with recommended joint activities to prevent future recurrence.

Background

International Rectifier ordered part number 1210X332K501NMH in June and December 2001 against their Source Control Drawing 608X-0100, Revision C. Product was supplied from the same manufacturing lot for both purchases. Their drawing specifies a Novacap catalog part number with an "N" termination suffix. Per the Novacap 11/00 catalog the "N" suffix indicated a nickel barrier termination with a 90%Sn/10%Pb solder overcoat.

The majority of capacitor users were transitioning to 100%Sn requirement for environmental compliance in the same time frame. Novacap assembled new lines for ramp up to 100%Sn and began transition of older lines to 100%Sn while maintaining a single line of the lower volume Sn/Pb requirements.

A cursory review of MIL-PRF-38534 revisions indicate the prohibition of pure tin in internal element finishes and definition of pure tin as containing less than three percent alloy (Pb) material were not called out until the January 2003 revision E. The previous revision required device finishes to contain a minimum of 2% Pb by weight. (Para. E.4.2.7) The review of the MIL-PRF-123 revision history showed the requirement for a minimum of 4% Pb in the Sn/Pb alloy of the solder overcoat of a barrier layer was implemented 5/30/01. Novacap received a copy of this revision in October 2002. (Novacap implemented internal CA to assure latest revisions of external specifications are maintained)

GIDEP Problem Advisory FV5-P-05-01 goes into greater detail regarding the various measurement techniques and their correlation in regards to electroplated Sn/Pb compositions and process controls.

Investigation

Novacap implemented new plating tanks for 100%Sn in 2000 and transitioned others to 100%Sn while maintaining one line for Sn/Pb. Customers with known requirements for Sn/Pb (90/10 or minimum 2% Pb) were controlled based on specification comments and processed through the Sn/Pb line. All others were processed through the 100%Sn lines with low levels of Pb present in the two converted lines. The “N” suffix was changed to mean nickel barrier during the transition with the required overcoat composition controlled by specification.

Novacap catalogs are updated periodically with a greater emphasis on updating the website which contains the catalog as well as other pertinent information. Page 5 of the catalog provides the website address and informs of the intent to maintain updates on the website as Novacap reserves the right to make changes to the catalog offerings. There was no printed catalog revision until 6/04. The “Y” termination was created for 90%Sn/10%Pb in February 2002.

The lot in question was processed in a converted line with low residual levels of Pb remaining. There was no indication on the traveler of the Pb requirement. Samples of the suspect parts were measured in various locations with XRF after removal from the customer’s boards. The results ranged from 1.1% to 3.3% Pb which was consistent with the low level Pb in the tanks based on bath chemistry records. However, it is not known what effect the installation and subsequent removal may have had on the consistency of this measurement.

There have been no reported failures of Novacap products due to tin whisker growth.

Summary

The increased level for required percentages of Pb was evolving in the same time frame as the worldwide transition to 100%Sn. The specifications for the suspect part number relied on the Novacap catalog part number, which did change, and did not call out the required solder composition. There was no formal notification to all customers and any website communications of the “N” suffix change were ineffective in this instance. The product in question was manufactured in 2001 prior to the MIL specification standards that it was measured against in 2005.

As an Application Specific supplier Novacap maintains a specification control system that is keyed by the customer and customer part number. The specific requirements as detailed by the customer are loaded into computer files that control the design and processing steps of each part number. These files are intended to allow for specified requirements above and beyond the catalog to assure conformance of the manufactured and supplied products. This system is dependent on the customer’s detailing their specific requirements in their communications and controlled documents as well as Novacap review and entry.

Containment Actions

The suspect time period identified during this investigation is January 2000, when 100%Sn plating lines were implemented and February 2002 when the 90%Sn/10%Pb suffix was created. It is possible that other products are affected from this time frame. We continue to supply many customers with the “Y” terminated products with improved controls over the solder composition to assure conforming measurement while taking into account the correlation of the various measurement techniques used in the industry.

Novacap is prepared to analyze any suspect samples or review process records for any parts of concern from the suspect period or any product produced since.

Any customer requiring 90%Sn/10%Pb products should review their specification to assure their requirements are clear and have been provided to Novacap. If the requirements are spelled out or if they are relying on the catalog part number the specification should be revised and provided to Novacap for entry into our specification system to assure conforming product supply.

Sincerely,

Brian D. Crowley
QA Manager

FV5-P-06-01A (Amendment)

Section 20 (continued)

Table I - Affected Hybrids with Pure Tin

Part Number	D/C
8486*S 'CH' AFL2805SW/CH	0514
8486*S 'CH' AFL2805SW/CH	0532
8486*S 'CH' AFL2805SW/CH	0441, 0517
8487*S 'CH' AFL2806R5DW/CH	0424
8487*S 'CH' AFL2806R5DW/CH	0424
8487*S 'CH' AFL2806R5DW/CH	0441
8487*S 'CH' AFL2806R5DW/CH	0523
8487*S 'CH' AFL2806R5DW/CH	0526
8488*S 'CH' AFL2820SX/CH	0419, 0423, 0435
8488*S 'CH' AFL2820SX/CH	0521, 0522, 0523
8870*B 'CH' AFL2808SX/CH	0505, 0510
8870*B 'CH' AFL2808SX/CH	0523, 0528
8933*B 'CH' AFL2808SX/CH	0429, 0438, 0444
8958*B (AHF2815TF/CH	0407
AFL27028SX/CH	0123
AFL27028SZ/CH	0121, 0122
AFL2803R3SX/CH	0448
AFL2805SX/CH	0448
AFL2805SX/CH	0448
AFL2805SZ/CH*B	0409
AHF2815T/CH	0243
AHF2815T/CH	0143
AHF2815TF/CH	0238
AHF2815TF/CH	0142
AHF2815TF/CH	0128, 0129
ASA2812D/CH*B	0422, 0433
M3G2803S	0310
M3G2803S	0310
M3G2805D	0229
M3G280505T-HY	0244
M3G2812D	0230
M3G283R415T	0235, 0244
M3G5005S-R01	0206
M3G5015D-R01	0146, 0147, 0206
M3G7015D	0330
M3GX280552S	0215, rwk 0225
M3GX2815D	0215
M3H280515T	0249
M3H2805S	0246
M3H2805S	0310
M3L2803S	0319
S2803R3S	0324
S2805S	0333

Tin Whisker Growth Experiment

Tin Whisker Growth:

The result from published field observations and tin whisker growth experiments provide some information on the tin whisker growth phenomenon and resultant influences. One such study was published in the official proceedings of the American Electroplaters and Surface Finishers Society, AESF SUR/FIN 2002, June 24 – 27¹. The paper examines whisker growth with multilayer ceramic chip (MLCC) capacitors. Although all factors that influence tin whisker growth on MLCC capacitor's end-terminations are not fully understood, the data suggests that the following MLCC capacitor construction characteristics and processes can limit the growth of tin whiskers.

- 1- Large (> 5um) and well-polygonized tin grain structure
- 2- Thick tin-plated layer (~5 – 10um)
- 3- Electroplated Nickel barrier layer (at least 2um thickness) to limit silver diffusion into the tin
- 4- Post-electroplate annealing processes to help reduce residual stress in the tin layer

Previous experiments conducted by IR on similar MLCC capacitors² demonstrated that the growth of tin whiskers was precipitated by thermal cycling and that the formation of tin whiskers on the capacitors end-terminations was limited. Tin whisker growth length measured up to 20.5 um following 100 and 200 temperature cycles. While the growth length was limited, the whisker growth density (whisker population) was observed to increase between SEM inspections at 100 and 200 temperature cycles.

Current Experiment:

In an effort to better understand the effects and risks associated with the DC/DC hybrids incorporating the MLCC (Multilayer Ceramic Chip) capacitors with pure tin plated end-terminations, IR conducted experiments by subjecting devices to thermal conditioning. The evaluation consisted of subjecting hybrid microcircuits (Date Code 0128 and 0129) through temperature cycle, and followed with capacitor termination inspection - specifically for the presence of tin whiskers and growth thereof. Note that the hybrids were finished products - processed and pre-conditioned to MIL-PRF-38534, Class H screen.

Evaluation Steps:

- 1) Confirm the presence of pure tin (Sn) and nickel (Ni) barrier layer on subject capacitor end-termination (sample cross section, SEM-EDS analysis)
- 2) Conduct initial inspection of the end-terminations using SEM method.
- 3) Condition the test samples with temperature cycle (100 cycles from -65C to 150C)
- 4) Conduct post 100TC inspection of the terminations using SEM method.
- 5) Condition the test samples with additional temperature cycles (100 cycles from -65C to 150C)
- 6) Conduct post 200TC inspection of the terminations using SEM method.

Result Summary:

Capacitor samples were cross sectioned and construction was validated with a Nickel Barrier layer under the plated tin. The plated tin layer measured ~ 10um and the Ni barrier layer measured ~ 10um, which should act to limit the growth of whiskers. No tin whisker growth was observed on the capacitors in the hybrids as received (pre-conditioned to MIL-PRF-38534, Class H screen). Following submission to 100 Temperature Cycles (-65C to 150C), no distinct whisker growth was observed. Following a total of 200 Temperature Cycles (-65C to 150C), the presence of tin whiskers was observed on the capacitor terminations. Tin whiskers measured in the range of 15 to 20um in length. Reference photo 1 and 2 herein for exhibit.

¹ <http://nepp.nasa.gov/whisker/experiment/exp5/index.html>

² GIDEP FV5-P-05-01A

Section 20 (continued)

Experiment Conclusion:

Based on results obtained from the experiments conducted, we can infer that tin whisker growth on the ceramic chip capacitor terminations appears to be limited to 15 - 20 microns in length. This inference is based on and represents a limited test scenario. As such, International Rectifier cautions against drawing conclusions concerning risk, reliability or performance impact based on the experiment results represented herein.

SEM Inspection / Imaging:

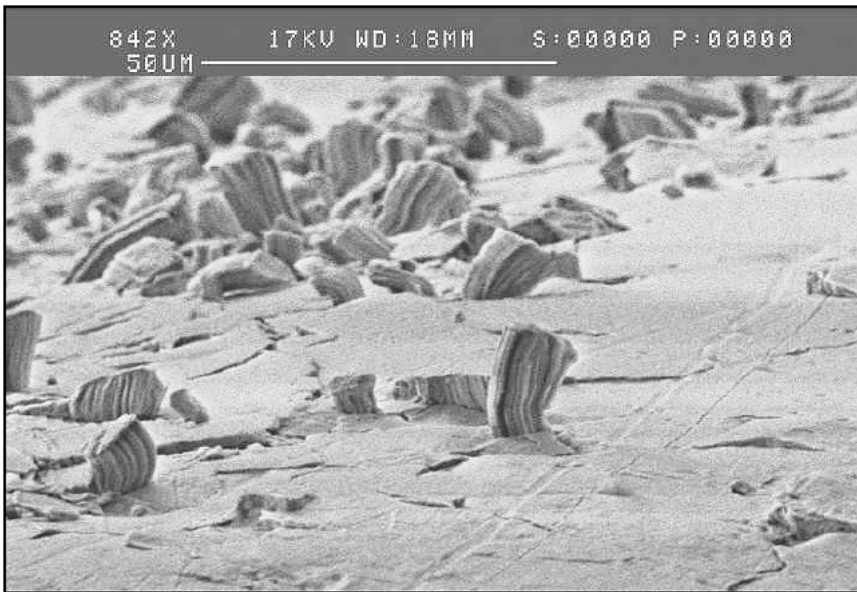


Photo 1 – Growth of Tin Whisker following 200 Temperature Cycles

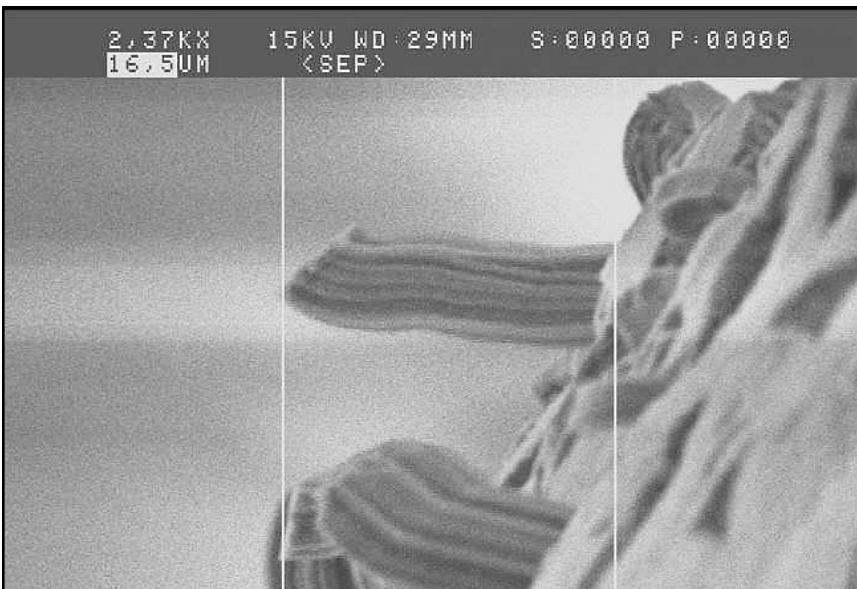


Photo 2 – Typical Tin Whisker ~ 16.5 micron in length (post 200TC)

Tin Whisker Observations on Pure Tin-Plated Ceramic Chip Capacitors

Jay Brusse, QSS Group, Inc., Greenbelt, MD

Tin whiskers pose a risk of intermittent short circuits in terrestrial applications and potentially catastrophic metal vapor arcs in space (vacuum) environments. Research on whiskering and mitigation practices has historically produced highly variable results. These variations and recurring observations of whisker-induced failures are central to the high reliability community's reluctance to adopt lead-free finish alternatives, especially pure tin, until clear evidence exists that the factors affecting whisker growth are understood and can be controlled. This paper highlights the variability of previous claims using recent observations of greater than 200- μ m-long whiskers on pure tin-plated ceramic chip capacitors as a classic example of the confounding nature of this phenomenon.

For more information, contact:

Jay Brusse
Senior Components Engineer
QSS Group, Inc. at NASA Goddard Space Flight Center
Bldg. 22, Room 036
Greenbelt, MD 20771
Jay.A.Brusse.1@gsfc.nasa.gov

This paper was presented at the Electronics Finishing II Session and published in the official proceedings of AESF SUR/FIN 2002, June 24 - 27, sponsored by the American Electroplaters and Surface Finishers Society, Orlando, FL.
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Introduction

Electroplated tin (Sn) and tin alloy finishes are used extensively in the electronic components industry to enhance solderability and corrosion resistance of electrical conductors made from metals such as copper, phosphor bronze and alloy 42 (iron-nickel alloy). Additional benefits of tin finishes include excellent control and uniformity of plating thickness (especially critical in miniaturized, fine-pitched components), good electrical conductivity, and non-toxicity¹. "Bright" finishes (those using organic additives in the plating bath to produce fine grain finishes) are also commonly used because they maintain an aesthetically pleasing shiny surface. For all of these purposes, a wide assortment of pure tin and tin-lead (2% to 50% Pb) based electroplated finishes have been used with much success². Since the mid-1990's, the United States (U.S.) military and other high reliability users have preferred tin-lead based finishes (minimum 3% Pb) over pure tin due to documented failures resulting from whisker growths on tin-plated components. These failures will be briefly described later.

Legislative pressures in recent years (particularly in Japan and Europe) have pushed the electronics industry to consider methods of eliminating Pb from their products and manufacturing processes³. Generally speaking, these rules are being developed in response to potential environmental and health hazards that may result from the manufacturing and disposal of consumer products bearing Pb and other hazardous materials. Although the U.S. is lagging Japan and Europe in introducing similar legislative restrictions, changes made by the U.S. Environmental Protection Agency (EPA) in 2001 now require industries to formally report any manufacture, use and/or disposal of Pb or Pb compounds in excess of 100 pounds per year⁴. Previously, the EPA reporting limit was as high as 25,000 pounds per year. These EPA rule changes and the desire to remain competitive in a world economy have prompted many U.S. electronic component manufacturers to explore alternatives to tin-lead based plating systems.

Recently, significant attention has been given to pure tin plating (especially "matte" finishes) as a candidate to replace widely used tin-lead finishes. With several factors in its favor, such as ease of converting existing tin-lead plating systems, ease of manufacture and compatibility with existing assembly methods, in addition to years of successful commercial use, pure tin plating is seen by many in the industry as a potentially simple and cost effective alternative⁵.

Despite the benefits, there remains one major impediment to the simple adoption of pure tin for use in high reliability applications: *many pure tin electroplates develop potentially damaging growths known as tin whiskers*. Assured methods for predicting if or when such growths may occur do not currently exist.

Hazards of Tin Whiskers

Figure 1 shows examples of tin whiskers on various types of pure tin-plated electronic components. As the name implies, tin whiskers are hair-like growths of near-perfect single-crystalline structures of tin that grow outward from some electroplated tin surfaces. Typical tin whiskers have diameters of only a few microns (μm) and have been reported to grow to lengths of several millimeters (mm), though lengths of less than 1-mm are typically reported^{1,6,7}. Their tiny dimensions make them extremely difficult to see unless proper illumination and/or high power inspection techniques are used such as scanning electron microscopy (SEM). In addition, troubleshooting of system problems may sometimes obscure whiskers as the root cause of failure if handling or probing is used which results in removal of the offending whisker.

Predicting if or when a particular tin-plated product will form whiskers has perplexed researchers and users alike for decades. Perhaps the most insidious traits of tin whiskers are the seemingly unpredictable nature of

their growth initiation and subsequent growth rate. Although several researchers have reported whisker incubation periods as short as a few days from the time of electroplating, others have documented incubation periods that have lasted many years^{8,9}. Growth rates as high as 9mm/year have been reported, but slower rates are much more common¹⁰. These attributes of whisker growth are particularly challenging because of the lengthy experiments (years) needed to establish meaningful results. Formally accepted test methods (environments) to accelerate whisker growth have not been established. Various industry groups in the U.S., Japan and Europe are presently working to develop such effective screening practices¹¹.

The primary concern with tin whiskers is that they are electrically conductive and can lead to short circuits in electronic assemblies. It is tempting to expect these tiny conductive filaments will fuse open at the instant they grow long enough to cause a short creating perhaps an unnoticeable "glitch" to the circuit performance. However, documented experiences have shown tin whisker failures also include permanent low current (few milliamps or less) shorts as well as catastrophic metal vapor arcs in vacuum capable of sustaining HUNDREDS of AMPERES¹². Table 1 categorizes the common failure modes attributed to tin whiskers while table 2 lists some notable examples of tin whisker induced failures in medical, military and space applications.

In previous work performed at the NASA Goddard Space Flight Center (GSFC), it has been demonstrated that tin whiskers (especially the longer, filament types) will bend in response to forces of electrostatic attraction (see Figure 2)¹³. Therefore, a tin whisker growing from a surface at one electrical potential will be attracted towards an adjacent conductor with a different potential. This behavior is highlighted to show that the probability of a tin whisker shorting event is greater than a "random" occurrence dependent solely on the whisker's direction of growth.

A) Ceramic Chip Capacitor Termination



B) Electromagnetic Relay Terminal



C) Hybrid Microcircuit Lid



D) Terminal Lug



E) Test Points



F) DIP Microcircuit Lead



Figure 1: Examples of Electronic Components with Tin Whiskers (All are Pure Tin-Plated)

Table 1: Equipment Failure Modes Induced by Tin Whiskers

Failure Mode	Description
<i>Permanent Short Circuits</i>	In low voltage, high impedance circuits there may be insufficient current available to fuse the whisker open. Fusing currents between 30 mA and 75 mA have been reported ^{8, 24} .
<i>Transient Short Circuits</i>	If the available current exceeds the fusing current of the whisker, the circuit may only experience a transient glitch as the whisker fuses open.
<i>Metal Vapor (Plasma) Arcing In Vacuum</i> ¹²	In vacuum (reduced atmospheric pressure) a much more destructive short circuit phenomenon can occur. If currents of above a few amps are available and the supply voltage is above approximately 18V, the whisker may vaporize creating a plasma of tin ions that can conduct Hundreds of Amperes . An adequate supply of tin from the surrounding plated surface can help to sustain the arc until the available tin is consumed or the supply current is interrupted such as occurs when a protective fuse or circuit breaker interrupts the current flow.
<i>Debris/Contamination</i>	Mechanical shock, vibration or handling may cause whiskers to break loose from the plated surface. Once free to move about, these conductive particles may then interfere with sensitive optical surfaces or the movement of microelectromechanical systems (MEMS). This debris may also lead to short circuits as noted above ²⁵ .

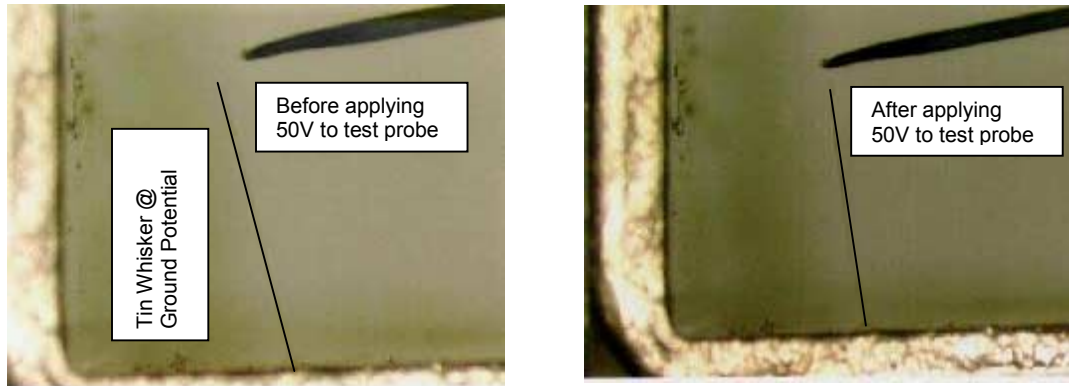


Figure 2. Tin Whisker "Bending" in Response to Electrostatic Attraction.

(Note: Photo has Been Enhanced to Aid in Observation of the Tin Whisker which is Difficult to See in this Optical Photograph)

Table 2: Reported Field Problems Induced by Tin Whiskers

Application	Problem
Medical	
Heart Pacemaker ²⁶	Class I Product Recall: Tin whisker shorts from pure tin-plated housing of crystal causes complete loss of pacemaker output.
Military	
F-15 Radar ²⁵	Tin whisker from pure tin-plated hybrid microcircuit lids.
U.S. Missile Program ²⁷	Tin whisker from pure tin-plated relays
U.S. Missile Program ^{28,29}	Tin whisker growing from pure tin-plated TO-3 transistor can shorts collector to case.
Phoenix Air to Air Missile ³⁰	Tin whisker shorts inside hybrid microcircuit.
Patriot Missile II ³¹	Tin whiskers from pure tin-plated terminals
Space (Satellite)	
GALAXY IV ^{32,33}	Complete loss of satellite operations. Tin whisker short (metal vapor arc in vacuum) from pure tin-plated relays
GALAXY VII ³³	Complete loss of satellite operations. Tin whisker short (metal vapor arc in vacuum) from pure tin-plated relays
SOLIDARIDAD I ³³	Complete loss of satellite operations. Tin whisker short (metal vapor arc in vacuum) from pure tin-plated relays
Additional Satellites ^{32,33}	Three additional satellites of the same general design as above have lost one of two redundant satellite control processors due to tin whisker shorts

Compressive Stress Theory of Whisker Growth

Although tin whiskers were first reported in the 1940's¹⁴, researchers still have not reached consensus to explain the mechanism(s) that drives whisker formation. However, most experts concur that whisker formation is the result of a mechanical stress-relief process. More precisely, it has been postulated that the development of "compressive" stress in the tin layer provides the fundamental driving force for whisker growth^{2,15,16}. The following factors have been cited in the literature as potential contributors to compressive stress in the tin layer¹⁷:

- Substrate Element Diffusion (e.g., copper, zinc) into Tin Layer
- Intermetallic Compound (IMC) Formation between Tin and Substrate (e.g., Cu_6Sn_5)
- Plating Chemistry (e.g., Organic additives termed "brighteners" may increase the "as-plated" residual stress)
- Plating Process Parameters (e.g., higher current density may produce high residual stress)
- Contamination (organic and inorganic)
- Substrate Stress (e.g., stamping, cold working, swaging)
- Environmental Stresses (temperature, humidity)
- Externally Applied Mechanical Stress (e.g., torquing of a fastener, scratches from handling)

Lee and Lee¹⁵ have described one plausible model for whisker growth that is heavily reliant upon the processes of diffusion and intermetallic compound (IMC) formation occurring between the tin layer and the underlying substrate. In this model atoms with high diffusivity in tin (e.g., copper or zinc) migrate from the substrate material (phosphor bronze in their study) into the tin layer preferentially along the tin grain boundaries. The diffusing elements and the resulting IMCs (e.g., Cu_6Sn_5) occupy space within the tin crystallographic structure producing a resultant compressive stress in the tin layer. As the stress increases with time, the thin, brittle tin oxide layer that forms over the electroplated tin can rupture. Once the oxide layer has ruptured, tin grains (whiskers) may then be extruded in a continuous fashion through the oxide layer as a means of relaxing compressive stresses. As growth proceeds, the localized compressive stress tends to reduce until the stress level is no longer sufficient to support further growth (equilibrium).

Based upon "compressive" stress theories such as this one, the majority of whisker mitigation practices being explored today involve attempts to minimize or eliminate those factors which encourage the development of compressive stresses within the tin electroplate. Multilayer ceramic capacitors (MLCCs) are one example of tin-plated components where whisker mitigation practices used in their manufacture have been studied and reported.

Multilayer Ceramic Capacitor Construction

Today, MLCCs are utilized in almost every electronics application. Annual production of MLCCs exceeds several billion capacitors worldwide. MLCCs come in a wide range of capacitance/voltage ratings and package configurations (leaded and surface mount). Surface mount chip MLCCs are by far the most popular package configuration and are currently available with footprints as small as 0.5-mm x 0.25-mm (0.02-in. x 0.01-in.). Figure 3 illustrates the typical construction of a ceramic chip capacitor designed for installation by soldering methods.

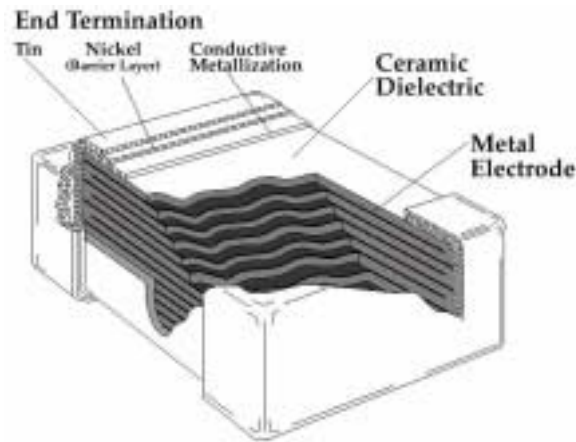


Figure 3. Typical Cutaway View of a Tin-Plated Multilayer Ceramic Chip Capacitor (Kemet Electronics Corp Data Sheet)

The capacitor element consists of layers of a ceramic-based dielectric material (e.g., barium titanate) with metal electrodes (e.g., palladium-silver [Pd-Ag]) interleaved. The ceramic-electrode structure is sintered at high temperature to form a homogenous block. Conductive terminations are applied to the ends of this ceramic block to provide electrical contact points for this surface mountable device. The end termination structure typically consists of three distinct layers as follows:

1. **Silver Frit:** Silver paste (with some glass frit mixed in) fired onto the ceramic to make electrical connection to the electrode layers. The glass facilitates mechanical adhesion to the ceramic.
2. **Nickel:** Typically electroplated $\sim 5\text{-}\mu\text{m}$ thick applied over the silver frit to minimize diffusion of silver into the final solderable finish.
3. **Tin or Tin-Lead Alloy:** Typically electroplated $\sim 5\text{-}\mu\text{m}$ to $10\text{-}\mu\text{m}$ thick over the nickel barrier layer to provide a solderable final finish.

Note: For wire bonding or conductive adhesive (e.g., silver epoxy) attachment applications, MLCCs with gold or Pd-Ag final finishes are also available instead of tin or tin-lead.

Previous Whisker Research on MLCCs

In the vast research conducted to date on tin whiskers, there are only a few studies that directly discuss tin whisker growth in passive electronic components such as MLCCs¹⁸⁻²¹. These papers generally proclaim MLCCs to be immune to tin whisker formation based on a number of characteristics of the MLCC termination structure. Selcuker¹⁸ and Endo¹⁹ have categorized the following attributes of the typical MLCC construction and manufacturing processes as factors that reduce their propensity to form whiskers:

1. "Large" ($>5\text{-}\mu\text{m}$) and well-polygonized tin grain structure
2. "Thick" tin-plated layer ($\sim 5 - 10\text{-}\mu\text{m}$)
3. Electroplated Nickel barrier layer (at least $2\text{-}\mu\text{m}$ thick) limits silver diffusion into tin and itself has very low diffusivity into tin.
4. Post-electroplate annealing processes help reduce residual stress in the tin layer and stimulate tin grain growth

Recent research employing pure tin finishes over nickel barrier layers over copper substrates has postulated that the nickel layer may also impart a "tensile" (rather than a compressive) stress to the tin layer²². If this is true, then theoretically such tensile stresses tend to discourage whisker extrusion.

In 1997 Endo¹⁹ reported 18 years worth of whisker-free observation of MLCCs stored continuously at 50°C. This storage condition was chosen because of previous whisker research citing 50°C as the optimal temperature for whisker formation to occur.

In 2001, the author of this present work surveyed five major domestic manufacturers of MLCCs to solicit their experience with tin whiskers²³. All of those surveyed indicated they knew of no formal reports of tin whiskers on any of their MLCC products.

Recent Experimental Observations of Tin Whiskers on MLCCs after Temperature Cycling

To illustrate the confounding nature of the tin whisker phenomenon, several recent examples of profuse tin whisker formation (some approaching 250-μm long) on pure tin-plated MLCCs (Figure 4) are described. The construction of these MLCCs is typical of the MLCC manufacturing industry and is comparable to MLCCs previously studied. Despite having most of the "whisker reducing" attributes described in previous studies^{18,19}, it will be shown that these capacitors formed tin whiskers when exposed to environmental test conditions different from those used in prior research.



Figure 4. Pure Tin-Plated MLCC from Manufacturer "A" with Tin Whiskers (Max. Whisker Length ~100-μm in this Image)

The following examples of tin whiskers on pure tin-plated MLCCs were collected from formal and informal exchanges of information with Original electronic Equipment Manufacturers (OEMs) who reported their observations to engineers at the NASA Goddard Space Flight Center (GSFC). As noted, some of the work has been augmented by examination at GSFC and independent experiments performed by The Aerospace Corporation¹⁷ to confirm the effects of environment reported by other researchers. In all cases, these examples are presented with due concern for the confidentiality of the OEMs involved.

Example #1: Pure Tin-Plated MLCCs from Manufacturer "A" in Hybrid Microcircuit

In 2000 an OEM ordered Pd-Ag terminated MLCCs from a major manufacturer (Manufacturer "A"). The MLCC manufacturer mistakenly supplied pure tin-plated capacitors due to a logistical error. The OEM's intent was to mount Pd-Ag terminated MLCCs onto gold plated mounting pads of a substrate inside a hermetically

sealed hybrid microcircuit. The OEM's mounting process employs a widely used silver-loaded conductive epoxy to accomplish the bond between the substrate pads and the MLCC terminations (Figure 5). Once assembled, the hybrids are hermetically sealed in a Nitrogen atmosphere. These assembly practices are commonly used in the manufacture of complex hybrid modules. However, the use of pure tin MLCCs with conductive epoxy is not a recommended practice due to potential mechanical and electrical integrity concerns when bonding epoxies to tin or tin-lead.

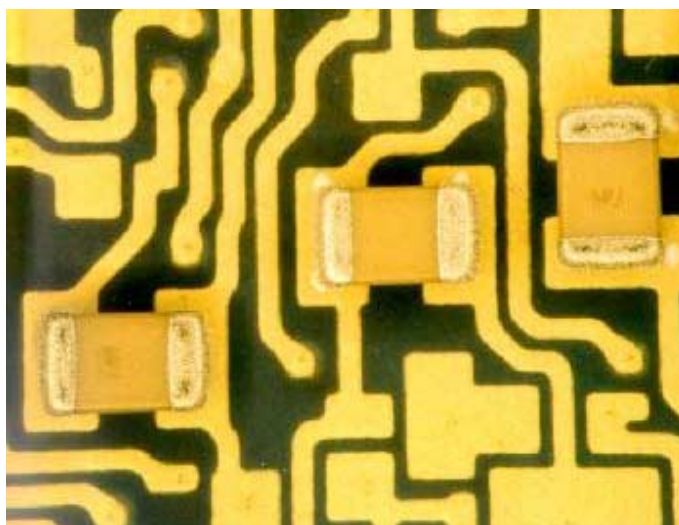


Figure 5: Optical View of Hybrid Package Substrate with Epoxy Mounted Pure Tin-Plated MLCCs (MLCC dimensions 2.03 mm x 1.27 mm)

Example #1: Description of Experiment

In the present example, the OEM was fortunate to catch the MLCC manufacturer's mistaken shipment of pure tin prior to issuing the MLCCs to the production floor. Rather than return the erroneous shipment for replacement, the OEM opted to conduct an evaluation to determine if the pure tin MLCCs may still meet the performance requirements of their application even when mounted using conductive epoxy. Therefore, the OEM manufactured and sealed several representative hybrid packages utilizing the pure tin-plated MLCCs and conductive epoxy mounting methods as described above. The hybrids were divided into two groups for the following environmental test conditions designed to evaluate the electrical and mechanical integrity of this bonding technique:

Condition 1: Thermal Cycle

-40°C to +90°C up to 500 cycles

Condition 2: High Temperature Storage

+90°C for 400 hours

The OEM removed representative hybrid packages from test at discrete intervals of time for inspection. The inspection test protocol consisted of delidding the hybrid to enable optical and scanning electron microscope (SEM) inspection of the capacitors, electrical conductivity measurements of the epoxy joints and mechanical strength measurements of the bond via die shear testing.

Example #1: Experimental Results

One representative hybrid package was removed from test Condition 1 (temperature cycling) after 300+ cycles. During visual inspection the user was completely surprised to find the entire termination surface of every

MLCC in the package was covered with a dense forest of “moss-like” tin whiskers with some approaching 100- μ m long (Figure 4).

Additional packages were inspected after 500 thermal cycles. The OEM reported whisker growths on the MLCCs were longer than those observed at the 300-cycle inspection point. At this point in the evaluation the OEM determined that the size and density of the tin whiskers observed were unacceptable for their densely packaged circuitry.

Hybrid packages subjected to test Condition 2 (high temperature storage) were inspected after 400 hours of test. Interestingly, there was no evidence of tin whiskers for specimens stored under this condition.

Incidentally, the user’s analysis of the bond integrity after Conditions 1 and 2 revealed less than desirable adhesion strength and electrical conductivity. These results also show that it is not advisable to use conductive epoxy mounting practices with pure tin-plated MLCCs. However, OEMs that do not have safeguards in place to protect against incorrect shipment of pure tin-plated MLCCs when Pd-Ag MLCCs are ordered may be prone to problems such as observed in Example #1.

Example #1: Effects of Additional Ambient Storage on MLCCs with Tin Whiskers

Component engineers at the NASA Goddard Space Flight Center (GSFC) learned of this OEM's dilemma through a technical email discussion forum catering to topics in electronics assembly. Intrigued by the OEM's experimental observations, GSFC engineers petitioned the user for representative MLCCs with tin whiskers to further document the whisker formations and analyze the MLCC termination construction. One hybrid package containing six epoxy mounted MLCCs was supplied for this analysis.

The OEM reported that the representative hybrid had been subjected to 200+ thermal cycles of test Condition 1. As received by GSFC, the hybrid package had already been delidded by the OEM for optical inspection only (i.e., the MLCCs were still epoxied to the substrate). Upon receipt of the hybrid, GSFC parts analysts performed SEM inspection. Figure 6 shows the profuse density of growths (as high as 800 per mm²) and maximum whisker lengths on the order of 100- μ m thus confirming the OEM's previous reports.

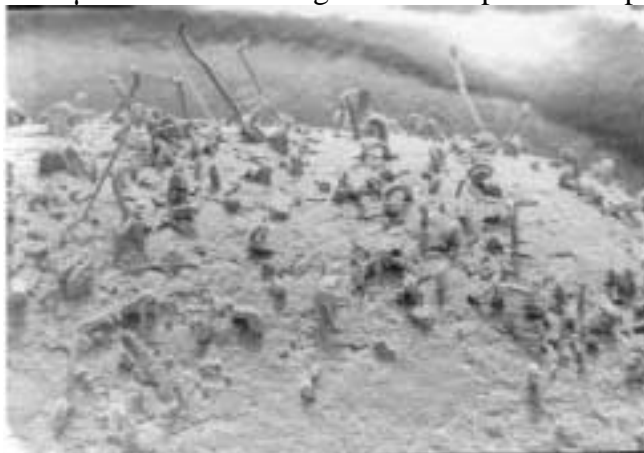


Figure 6: NASA GSFC SEM Inspection of Pure Tin-Plated MLCC from Manufacturer "A" with Tin Whiskers

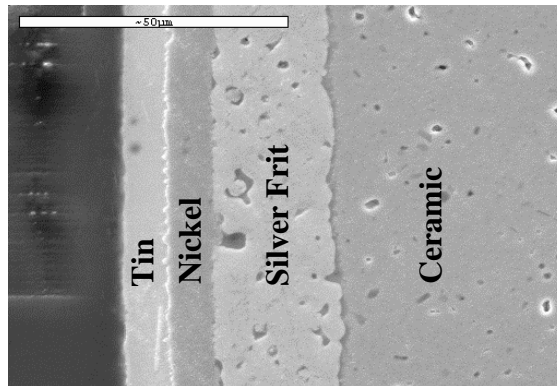


Figure 8: Cross-Section of MLCC Indicating the Thickness and Composition of the Various Layers

The bulk ceramic dielectric was observed to be of a commonly used barium titanate formulation.

The cross-sectioned MLCC was next chemically etched to highlight the grain structure of the tin electroplate. Though difficult to see in Figure 9, this analysis showed that the tin grains were polygonal structures ranging from 5 to 15- μm in size.

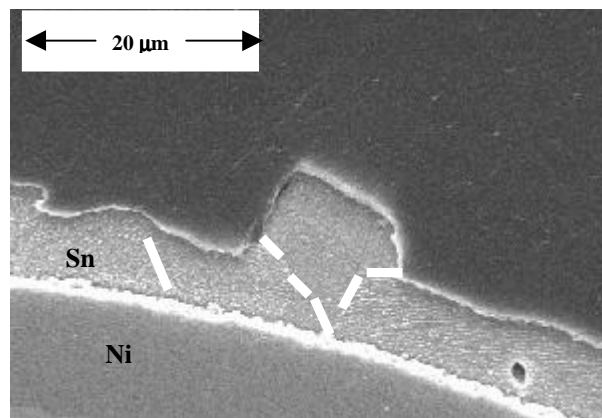


Figure 9. Tin Etch of Cross Section of MLCC Termination Structure with Grain Boundaries Highlighted

The results of the cross section analysis show that the attributes of this MLCC are essentially identical to those of the MLCCs reported to be whisker-free after 18 years in the earlier studies.

The most obvious difference between the experiments of the different researchers is the environmental test conditions chosen for judging whisker propensity. The more recent OEM's experiments described in Example #1 suggest that exposure to thermal cycle environments more actively excites the necessary stresses to spawn whisker formation in devices with a construction like MLCCs. Coefficient of thermal expansion mismatches among the various materials in the MLCC construction or possible IMC formation between nickel and tin are suspected to be potential factors contributing to whisker formation in this study. However, the exact mechanisms involved in these growths have not been determined.

Example #2: Thermal Cycle Evaluation of Pure Tin-Plated MLCCs from Manufacturers "B" and "C"

Having heard the reports of MLCCs with tin whiskers described by Example #1, The Aerospace Corporation performed experiments designed to validate these previous observations. Despite the fact that the OEM application would utilize reflow soldering of the pure tin MLCCs using Sn/Pb solder paste, the OEM was

concerned that portions of the capacitor terminations may remain unreflowed or tin-rich as some soldering processes do not guarantee complete wetting of the MLCC termination with Sn/Pb solder.

In a study to assess the risk of tin whisker formation, The Aerospace Corporation obtained pure tin-plated MLCCs from two different manufacturers ("B" and "C"). The MLCCs from Manufacturer "B" are typical commercial grade capacitors whose cross section is shown in Figure 10. Note that this lot of MLCCs contains a nickel barrier layer ~ 6- μm thick with a pure tin final finish ~10- μm thick

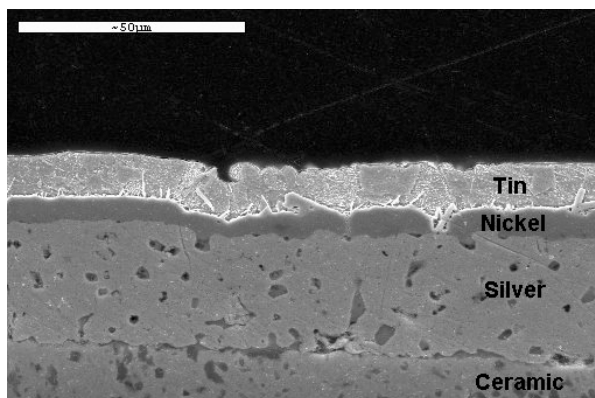


Figure 10. Cross-Section of Pure Tin-Plated MLCC from Manufacturer "B"
("Typical" MLCC Construction with Nickel Barrier Layer)

The production lot of MLCCs from Manufacturer "C" was also of similar construction; however, this lot was manufactured in accordance with U.S. Military specification requirements (MIL-PRF-55681).

To simulate the effects of reflow temperature exposure that might be expected from typical installation methods, several parts from Manufacturer "B" were exposed to temperatures of 215°C for 5 seconds without actual solder present. Additional samples from Manufacturer "B" and all of the samples from Manufacturer "C" were not subjected to this simulated reflow.

All of the capacitors were then subjected to the same environmental test described by Condition #1 of Example #1 (thermal cycle). Capacitors from Manufacturer "B" were examined after 500+ cycles and densely populated regions of tin whiskers up to ~25- μm in length were observed (Figure 11) even for those parts exposed to simulated reflow. The MLCCs from Manufacturer "C" were examined after 100 cycles and also showed evidence of whiskers ~25 to 30- μm long (Figure 12).

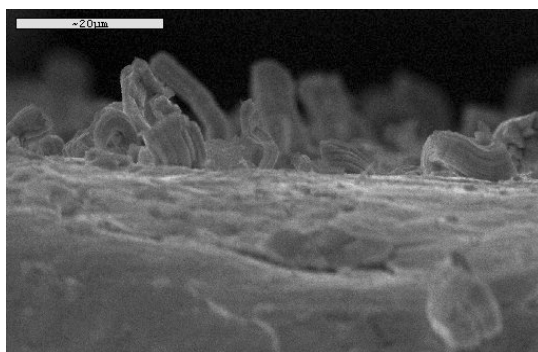


Figure 11. Tin Whiskers on Pure Tin-Plated MLCCs from Manufacturer "B" After Thermal Cycle

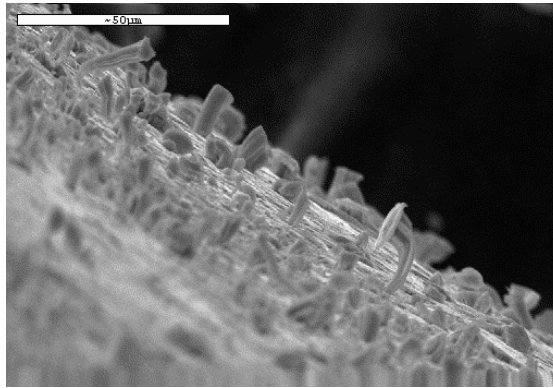


Figure 12. Tin Whiskers on Pure Tin-Plated MLCCs from Manufacturer "C" After Thermal Cycle

The results of these experiments confirmed most of the observations from Example #1. However, the maximum length of the whiskers found in Example #1 were from 3 to 10 times longer than those observed in Example #2. It will be interesting to see if the whiskers from Example #2 continue to grow under ambient storage as reported in Example #1.

Example #3: Tin Whiskers on Pure Tin-Plated MLCCs from Manufacturer "B" After Vapor Phase Installation and Thermal Cycle Testing

Many who have heard of the intriguing observations described in Examples #1 and #2 have suggested that these capacitors would not have whiskered if they had been installed using soldering techniques for which tin-plated MLCCs are designed. They cite references in the tin whisker literature that suggest the heat of the reflow process will relax residual stresses in the plating thought to be key contributors to whisker formation. They also cite references that suggest the introduction of Pb from the Sn-Pb solders commonly used to mount this type of capacitor would cover most of, if not the entire, MLCC termination surface thus introducing an alloying element (Pb) that is widely considered to have whisker inhibiting properties.

Circa 1997, a different OEM conducted experiments to determine the whiskering propensity of MLCCs after vapor phase installation at 217°C using Sn63/Pb37 solder paste. Pure tin-plated MLCCs of four different chip sizes all from Manufacturer "B" were assembled onto FR4 printed wiring boards using the OEMs standard vapor phase process.

Some of the assembled boards were then subjected to thermal cycle testing from -55°C to +100°C for several hundred cycles. SEM inspection was performed at discrete intervals to examine for evidence of tin whiskers.

Upon examination after 50 cycles, the two larger MLCC chip sizes showed evidence of tin whiskers (~10-μm long) growing from the tin-rich areas which were not wetted by Sn/Pb solder during installation. Additional cycling to 400 cycles showed evidence of tin whiskers approaching 30-μm long.

Example #4: Tin Whiskers on Pure Tin-Plated MLCCs from Ambient Storage

In 2000, the author heard anecdotal reports of pure tin-plated MLCCs from a fourth manufacturer (Manufacturer "D") that showed "moss-like" growths on the terminations of parts taken from stock. The OEM who made these observations reportedly had ordered Pd-Ag terminated capacitors for their hybrid epoxy mount application, but was supplied with pure tin-plated MLCCs by mistake (similar to the scenario described by Example #1). Unfortunately, formal documentation of this example has not been made available.

Conclusions

The observations and circumstances surrounding tin whisker formation on multilayer ceramic capacitors (MLCC) from four different manufacturers have been described. All of these observations provide a contrasting view of previously held beliefs that MLCCs are immune to tin whisker formation.

1. Surface mount multilayer ceramic capacitors (MLCCs) with electroplated pure tin finishes are not immune to tin whisker formation. Thermal cycling has been demonstrated to excite tin whisker formation in MLCCs with some documented growths approaching 250- μm .
2. Electrical shorting due to tin whiskers remains a significant problem. The incidence of tin whisker induced failures will increase in frequency with increased use of pure tin-plated electronic components and assemblies unless significant discoveries are made regarding effective mitigation practices.
3. Even when prohibited by system design and procurement requirements, components manufactured with pure tin finishes continue to appear in electronic equipment.
4. The factors that affect tin whisker formation are not fully understood. The author believes that there are also interactions amongst the various factors involved in whisker growth that makes research of this phenomenon difficult to control. Despite efforts to perform controlled experiments to judge whisker propensity, many subsequent experiments show "other" factors that can change previously held beliefs.

Recommendations

There are literally hundreds of published papers that discuss general and specific aspects of the tin whisker phenomenon. Reliance on simply a few of the more prominent publications on the subject could prove to be dangerous to manufacturers and users of electronic components and assemblies requiring the utmost in long-term reliable performance of their products. Despite the fact that this phenomenon has been known for more than 50 years, all must be aware of its confounding and sometimes conflicting nature.

1. The electronics manufacturing industry must strive to achieve and verify through direct investigation a consensus understanding of the fundamental mechanism(s) of whisker formation.
2. "Accelerated" test method(s) to judge a product or process's propensity to grow tin whiskers need to be developed. Methods must be tailorable to assess a wide variety of electronic component constructions and user application conditions including factors such as temperature, temperature change, humidity, and pressure.
3. Experiences with tin (and other metal) whiskers need to be shared more openly in order to create an environment conducive to resolution of this dilemma.
4. OEMs and electronic component manufacturers should adopt practices to assess and mitigate the risk of tin whisker induced failures in their products.

Acknowledgments

The author would like to thank Mike Sampson, Dr. Henning Leidecker (NASA Goddard) and Jong Kadesch (Orbital Sciences Corporation) for their support and contributions to this paper. In addition, the support of Dr. Gary Ewell and Jocelyn Siplon (The Aerospace Corporation) is greatly acknowledged. Finally, the author expresses his gratitude to Ingemar Hernefjord for valuable discussions.

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