PD - 94665

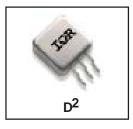
International **ICR** Rectifier

Adjustable Positive Linear Regulator Surface Mount (D²)

Product Summary

Part Number	Output Voltage	Output Current
OM7585ASR	+1.5V to +5.5V	5.0A

OM7585ASR 5962 - 0323703MUA



Description

The OM7585ASR is an adjustable positive linear regulator with a 5A maximum current capability. This part is specifically designed for low voltage applications where fast transient response is required. Utilizing a 3-pin surface mount package configuration, these devices are ideally suited for military/aerospace applications and other harsh environmental extremes.

Features

- Fast Transient Response
- Current Limit Protection
- Thermal Protection
- Hermetic D² Package
- DSCC Qualified
- Screened to MIL-PRF-38535

Absolute Maximum Ratings

Parameter	Symbol	Value	Units
Output Currrent	۱ ₀	5.0	A
Input Voltage	V _{IN}	7.0	V
Power Dissipation @ Tc = 25°C	PD	26.5	W
Thermal Resistance, Junction to Case	$R_{\theta JC}$	5.0	°C/W
Operating Junction Temperature Range	TJ	-55 to +125	
Storage Temperature Range	T _{STG}	-65 to +150	°C
Lead Temperature Soldering (10second maximum)	ΤL	300	

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Parameter	Symbol	Test Conditions	Min.	Max.	Units
Deference Veltage (N	N/	V _{IN} = 4.25V, I _{OUT} = 10mA, 25°C	1.238	1.266	V
Reference Voltage (Note 1)	V _{REF}	$2.75V \le V_{IN} \le 7.0V, I_{OUT} = 5.0A$	$5V \le V_{\text{IN}} \le 7.0V, \ I_{\text{OUT}} = 5.0A$ 1.20 1.2		
Line Regulation (Note 2)	V _{RLINE}	$2.75V \le V_{IN} \le 7.0V, I_{OUT} = 10mA$	-	8.5	
Load Regulation (Notes1 & 2)	M	$V_{IN} = 4.25V, \ 10mA \le I_O \le 5.0A, \ 25^{\circ}C$	- 24 n		mV
	V _{RLOAD}	V_{IN} = 4.25V, 10mA $\leq I_O \leq$ 5.0A, -55°C, 125°C	-	34	
Dropout Voltage (Note 3)	V _{DROP}	$\triangle V_{REF} = 1\%$, $I_{OUT} = 5.0A$	-	1.4	V
Current Limit	I _{SC}	V _{IN} = 6.75V	5.0	-	А
Adjust Pin Current		V _{IN} = 4.25V, I _{OUT} = 10mA	-	120	
Adjust Pin Current(Note 4)	I _{ADJ}	$2.75V \le V_{IN} \le 7.0V$, $10mA \le I_{OUT} \le 5.0A$	-	5.0	μA
Minimim Load Current (Note 5)	I _{MIN}	$2.75V \le V_{IN} \le 7.0V$	-	10	mA
Ripple Rejection	$\triangle V_{IN} / \triangle V_{OUT}$	f = 120Hz, C _{OUT} = 100μF tant,	60	200	-ID
		V _{IN} = 4.25V, I _{OUT} = 5.0A,	60	200	dB
Thermal Regulation @ 25°C	V _{REG}	$V_{IN} = 7.0V$, $I_{OUT} = 5.0A$ Pulse Width = 30ms, $P_D = 26.5W$	-	0.02	%/W

Electrical Characteristics -55°C \leq T_A \leq 125°C (Unless Otherwise Specified)

Notes

- 1. Low duty cycle pulse testing with Kelvin sense connections is required in order to maintain accurate data. Load regulation and output voltage are measured at a constant junction temperature.
- Line and load regulation are guaranteed up to maximum power dissipation. Power dissipation is determined by input/ output differntial and the output current. Guaranteed maximum output power will not be available over the full input/ output voltage range.
- 3. Dropout voltage is defined as the minimum differential voltage between V_{IN} and V_{OUT} required to maintain regulation at V_{OUT}. It is measured when the output voltage drops 1% below its nominal value.
- I_{FULL LOAD} is defined as the maximum value of output load current as a function of input-to-output voltage.
 I_{FULL LOAD} is equal to 5A for the OM7585ASR. The OM7585ASR has a constant current limit with changes in input-to-output voltage.
- 5. Minimum load current is defined as the minimum current required at the output in order for the output voltage to maintain regulation. The resistor values selected for the voltage divider automatically maintains this current.

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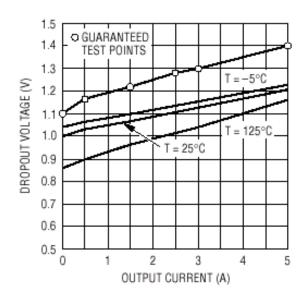


Fig 1: Typical Dropout Voltage Vs Output Current

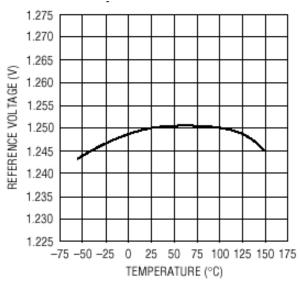
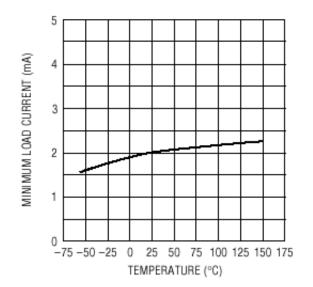


Fig 2: Typical Reference Voltage Vs Temperature



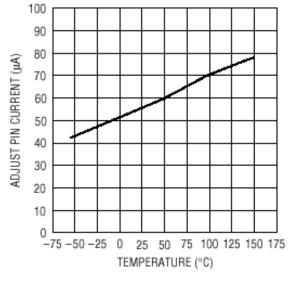


Fig 3: Typical Minimum Load Current Vs Temperature

Fig 4: Typical Adjust Pin Current Vs Temperature

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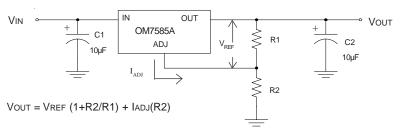
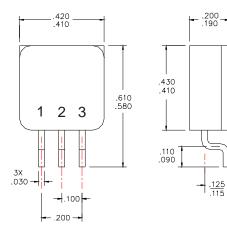


Fig 5: Typical Application

Layout Consideration

It is recommended that output capacitors be located as close as possible to the V_{OUT} terminal of the device to prevent any high frequency oscillation that may result due to excessive stray inductance.

Case Outline and Dimensions — D^2



Package Pin Description				
Pin #	Pin Symbol	I Function		
1	ADJ	A resistor from this pin to the V _{OUT} pin and ground sets the output voltage.		
2	V _{OUT}	The output of the regulator. A minimum of 10µF capacitor must be connected from this pin to ground to ensure stability.		
3	V _{IN}	The input pin of the regulator. Typically a large storage capacitor is connected from this pin to ground to ensure that the input voltage does not sag below the minimum drop out voltage during the load transient response.		

Part Numbering Nomenclature

<u>OM</u>	<u>7585A</u>	<u>S</u>	<u>R</u>	M
Omnirel	Device	S=Isolated	Package	Screening

International

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