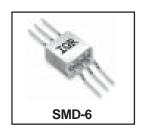
International Rectifier

Ultra Low Dropout, 7.0 A Adjustable Positive Linear Regulator Surface-Mount (SMD-6)

OM7580SM 5962 - 0323701MXA

Product Summary

Part Number Output Voltage		Current	Dropout
OM7580SM	+1.8V to +5.5V	7.0A	0.54V



Description

The OM7580SM is a 7.0A , ultra low dropout, adjustable linear regulator specifically designed for low voltage, high current applications. Housed in a hermetic package, the dropout of this device is 540mV at full load and as low a 100mV at light loads. The low dropout is achieved by an additional low current input voltage. This unit is ideally suited for military/defense, commercial aircraft, industrial control and other harsh environments where a hermetically sealed package is required.

Features:

- Dropout Voltage of 540mV at Full Load
- Dropout Voltage of 100mV at Light Loads
- Fast Transient Response
- Adjustable Output: 1.8 to 5.5V
- Remote Sense
- Hermetic SMD-6 Package ensures High Reliability

Absolute Maximum Ratings

Parameter	Symbol	Value	Units	
Output Currrent	I _O	7.0	А	
Power Input Voltage	V_{PWR}	6.0	V	
Control Input Voltage	V _{CTRL}	13		
Power Dissipation @ Tc = 25°C	P _D	20	W	
Thermal Resistance, Junction to Case	$R_{ heta JC}$	5.0	°C/W	
Operating Junction Temperature Range	TJ	-55 to +125		
Storage Temperature Range	T _{STG}	-65 to +150	°C	
Lead Temperature Soldering (10second maximum)	TL	300		

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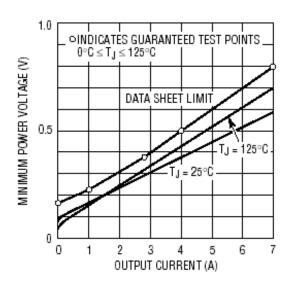
Electrical Characteristics @T_A = 25°C (Unless Otherwise Specified)

Parameter	Test Conditions	Min.	Тур.	Max.	Units			
Reference Voltage	V _{CTRL} = 2.7V, V _{PWR} = 2.0V, I _{LOAD} = 10mA	1.243	1.250	1.257	V			
V _{ADJ} = 0V	V _{CTRL} =2.7 to 12V, V _{PWR} =1.75V to 5.5V, I _{LOAD} =10mA to 6.0A ①				V			
Line Regulation	V_{CTRL} = 2.5 to 12V, V_{PWR} = 3.0V to 5.5V, I_{LOAD} = 1.0mA ①		1.0	3.0) mV			
Load Regulation					mv			
Minimum Load Current	V _{CTRL} = 5.0V, V _{PWR} = 3.3V, V _{ADJ} = 0V ⊕②		5.0	10				
Ground Pin Current	$V_{CTRL} = 5.0V$, $V_{PWR} = 3.3V$, $I_{LOAD} = 0$ mA ①		6.0	10	mA			
	V _{CTRL} = 2.75V, V _{PWR} = 2.05V, I _{LOAD} = 7.0A, TJ = 25°C			120				
Control Pin Current 3	V _{CTRL} = 2.75V, V _{PWR} = 2.05V, I _{LOAD} = 7.0A, TJ = 125°C			120				
	V _{CTRL} = 2.75V, V _{PWR} = 2.05V, I _{LOAD} = 6.0A, TJ = -55°C	-		130				
Adjust Pin Current	V _{CTRL} = 2.75V, V _{PWR} = 2.05V, I _{LOAD} = 10mA		50	120	μΑ			
V _{ADJ} = 0V								
Dinnle Dejection	$V_{CTRL} = V_{PWR} = 5.0V \text{ (AVG)}, V_{RIPPLE} = 1.0V_{P-P}, f = 120Hz$	60	80		dB			
Ripple Rejection	I _{OUT} = 4.0A TJ = 25°C							
Command Lineit	$V_{CTRL} = 2.75V$, $V_{PWR} = 2.05V$, $\Delta V_{OUT} = 100$ mV, $TJ_{=}25$ °C	7.1	8.0					
Current Limit	V_{CTRL} =2.75V, V_{PWR} =2.05V, ΔV_{OUT} =100mV, TJ =-55°C & TJ =+125°C	6.6			A			
	$V_{PWR} = 3.3V, I_{LOAD} = 7.0A, TJ = 25^{\circ}C$			1.33	V			
Minimum V _{CONTROL}	V _{PWR} = 3.3V, I _{LOAD} = 7.0A, TJ = 125°C			1.33				
	$V_{PWR} = 3.3V$, $I_{LOAD} = 6.0A$, $TJ = -55$ °C			1.35				
	V _{CTRL} = 2.75V, I _{LOAD} = 7.0A, TJ = 25°C			0.62				
Minimum V _{PWR}	V _{CTRL} = 2.75V, I _{LOAD} = 7.0A, TJ = 125°C			0.80	V			
	V _{CTRL} = 2.75V, I _{LOAD} = 6.0A, TJ = -55°C			0.80				
Thermal Regulation	V_{PWR} = 5.0V, I_{O} = 7.0A, $P_{D} \ge 20W$, pulse width = 30ms			0.02	%/W			
	Dropout is caused by either minimum control voltage or minimum power voltage. Both							
Dropout Voltage	parameters are specified with respect to the output voltage. The specifications represent the							
	minimum input/output voltage required to maintain 1% regulation.							

Footnotes

- ①- Denotes specifications which apply over the full operating temperature range.
- ②- The minimum load current is minimum current required to maintain regulation. Normally the current in the resistor divider used to set the output voltage is selected to meet the minimum load current requirement.
- ③- The control pin current is the drive current required for the output transistor. The control pin current is approximately 0.01% output current. The minimum value is equal to quiescent current of the device.

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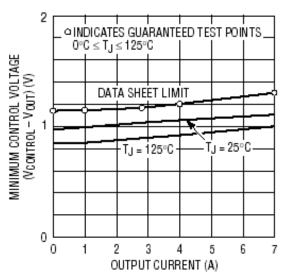
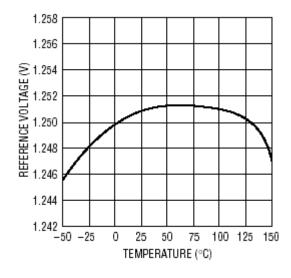


Fig 1: Typical Power Voltage Vs Output Current

Fig 2: Typical Control Voltage Vs Output Current



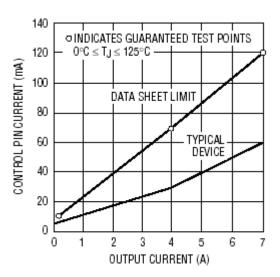


Fig 3: Typical Reference Voltage Vs Temperature

Fig 4: Typical Control Pin Current Vs Output Current

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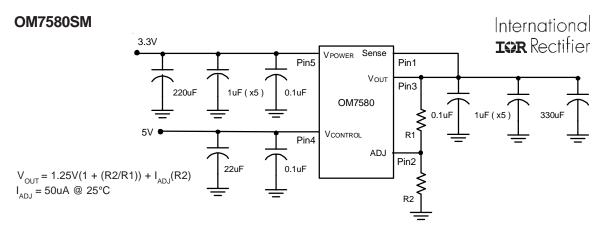
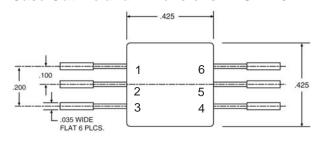


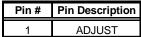
Fig 5: Typical Application

Layout Consideration

It is recommended that output capacitors be located as close as possible to the V_{OUT} terminal of the device to prevent any high frequency oscillation that may result due to excessive stray inductance. Specifications for capacitors: $330\mu F$ Tantalum Low ESR, $220\mu F$ Electrolytic, $22\mu F$ Electrolytic

Case Outline and Dimensions — SMD-6





Pin Assignment

Pin Description	
ADJUST	
V _{OUT}	
V_{CTRL}	
V_{PWR}	
N/C	
SENSE	

!	.350 MIN. + .020 + .115	
	115	
		.160
<u> </u>		
t ₀₂₀ .		080

Part Numbering Nomenclature

<u>OM</u>	<u>7580</u>	<u>s</u>	<u>M</u>	<u>X</u>
Omnirel	Device	S=Isolated	Package	Screening



WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, Tel: (310) 252-7105 IR LEOMINSTER: 205 Crawford St., Leominster, Massachusetts 01453, Tel: (978) 534-5776 Data and specifications subject to change without notice. 08/03