

Electrical Specifications ($-40^{\circ}\text{C} \leq T_A \leq +85^{\circ}\text{C}$ unless otherwise specified)

INPUT CHARACTERISTICS	Limits	Units
Min. Control Current (see figures 1 and 2)	10	mA
Max. Control Current for Off-State Resistance @ $T_A=+25^{\circ}\text{C}$	0.4	mA
Control Current Range (Caution: Current limit input LED, see figure 6)	5.0 to 25	mA
Maximum Reverse Voltage	6.0	V

OUTPUT CHARACTERISTICS		
Operating Voltage Range	0 to 60	V(AC peak)
Maximum Load Current 40°C (see figures 1 and 2)	1.0	A (DC)
Maximum Pulsed Load Current @ $T_A=+25^{\circ}\text{C}$ (100 ms @ 10% duty cycle)	2.4	A (AC peak)
Maximum Turn-On Time @ $T_A=+25^{\circ}\text{C}$ (see figure 7) For 500mA, 50VDC Load, 10mA Control	2.0	ms
Maximum Turn-Off Time @ $T_A=+25^{\circ}\text{C}$ (see figure 7) For 500mA, 50VDC Load, 10mA Control	0.5	ms
Maximum On State Resistance @ $T_A=+25^{\circ}\text{C}$ (pulsed) (See figure 4) 1.0A Load, 10mA Control	500	m Ω
Minimum Off State Resistance @ $T_A=+25^{\circ}\text{C}$ @ 48 VDC (see figure 5)	10^8	Ohms
Minimum Off-State dv/dt	1000	V/ μs
Output Capacitance (see figure 9)	150	pF @ 50 VDC

GENERAL CHARACTERISTICS	Limits	Units
Dielectric Strength, Input-Output	4000	V _(RMS)
Insulation Resistance, Input-Output , 90 V _{DC}	10^{12} @ $T_A=+25^{\circ}\text{C}$ - 50% RH	Ω
Capacitance, Input-Output	1.0	pF
Lead Temperature (1.6mm below seating plane) for 10 seconds	+260	$^{\circ}\text{C}$
Ambient Temperature Range:	Operating	-40 to +85
	Storage	-40 to +100

International Rectifier does not recommend the use of this product in aerospace, avionics, military or life support applications. Users of this International Rectifier product in such applications assume all risks of such use and indemnify International Rectifier against all damages resulting from such use.

Wiring Diagrams:

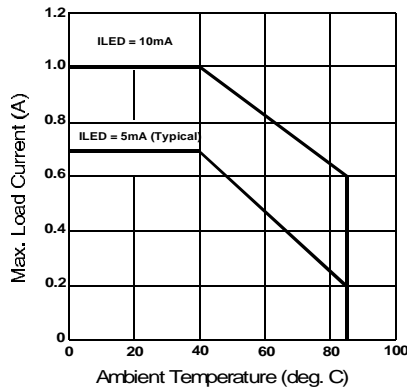
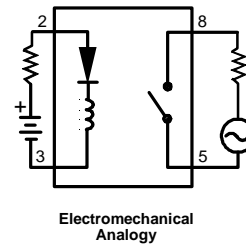
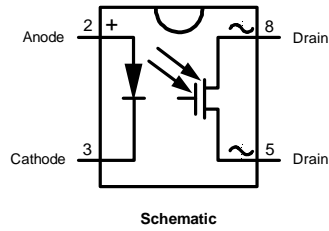


Figure 1. Current Derating Curves

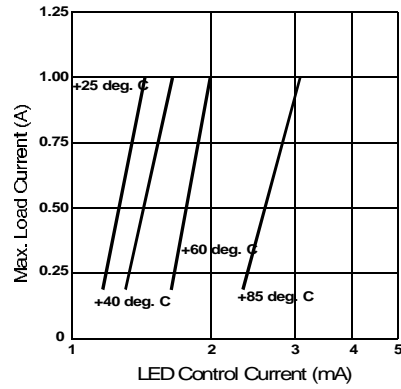


Figure 2. Typical Control Current Requirements

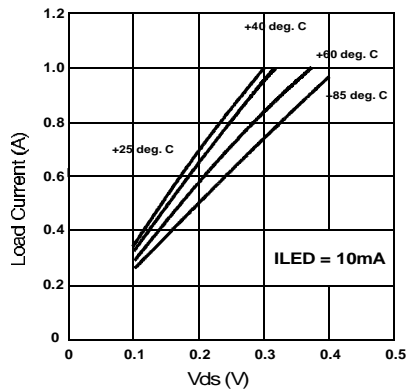


Figure 3. Typical On-Characteristics

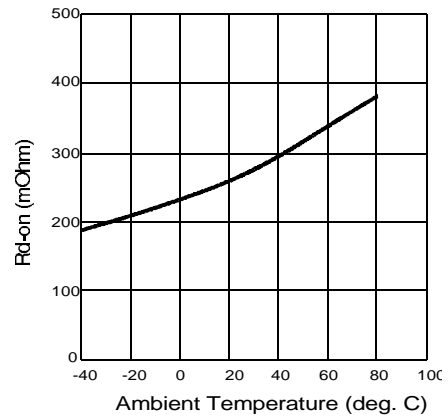


Figure 4. Typical On-Resistance

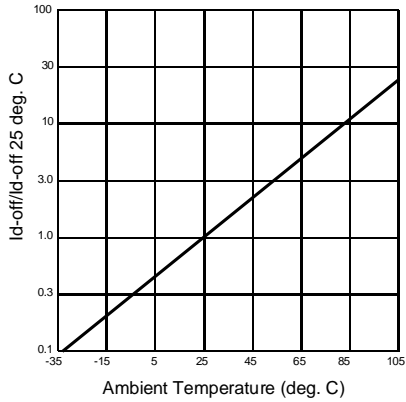


Figure 5. Typical Normalized Off-State Leakage

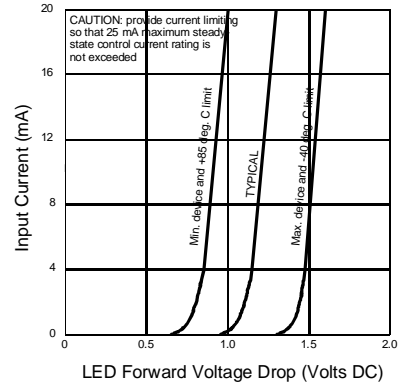


Figure 6. Input Characteristics (Current Controlled)

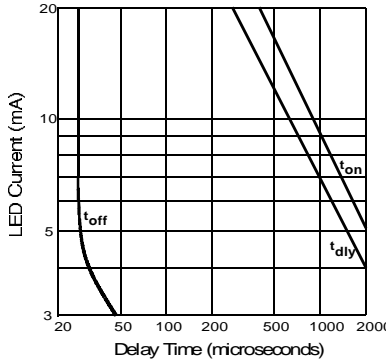


Figure 7. Typical Delay Times

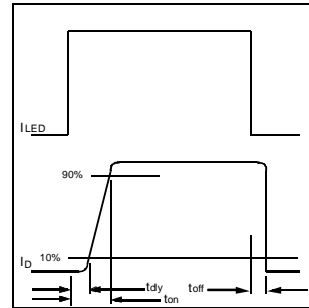


Figure 8. Delay Time Definitions

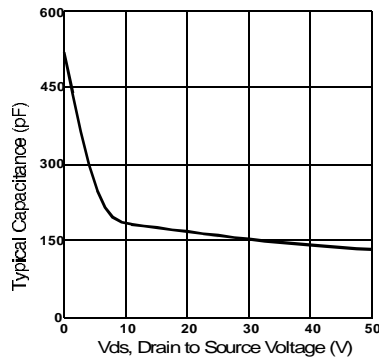
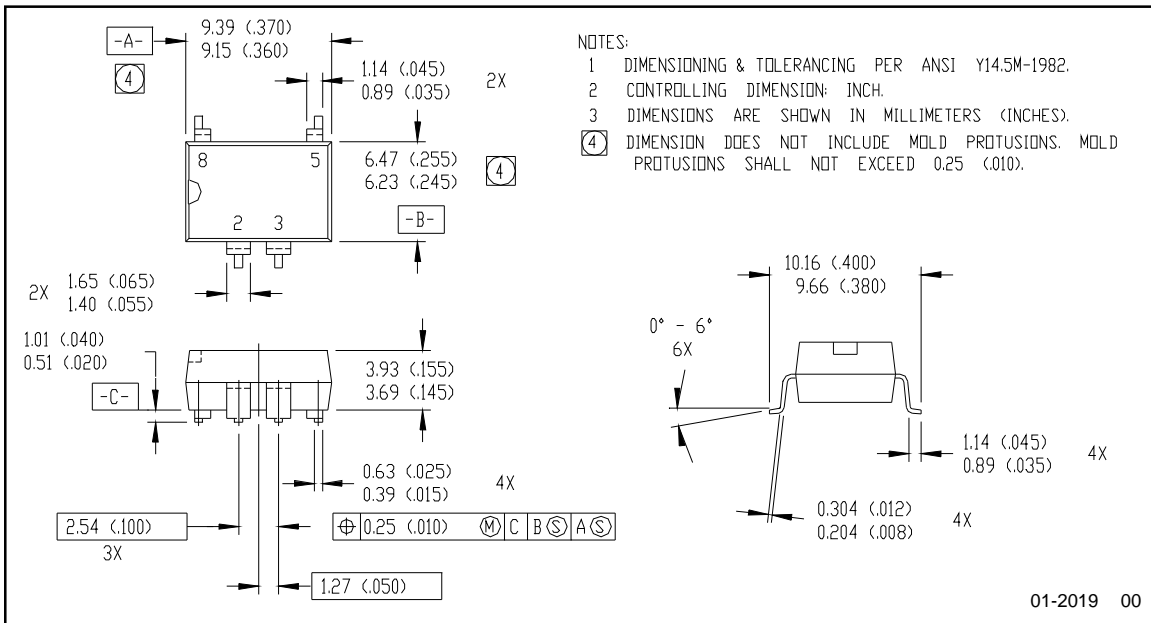
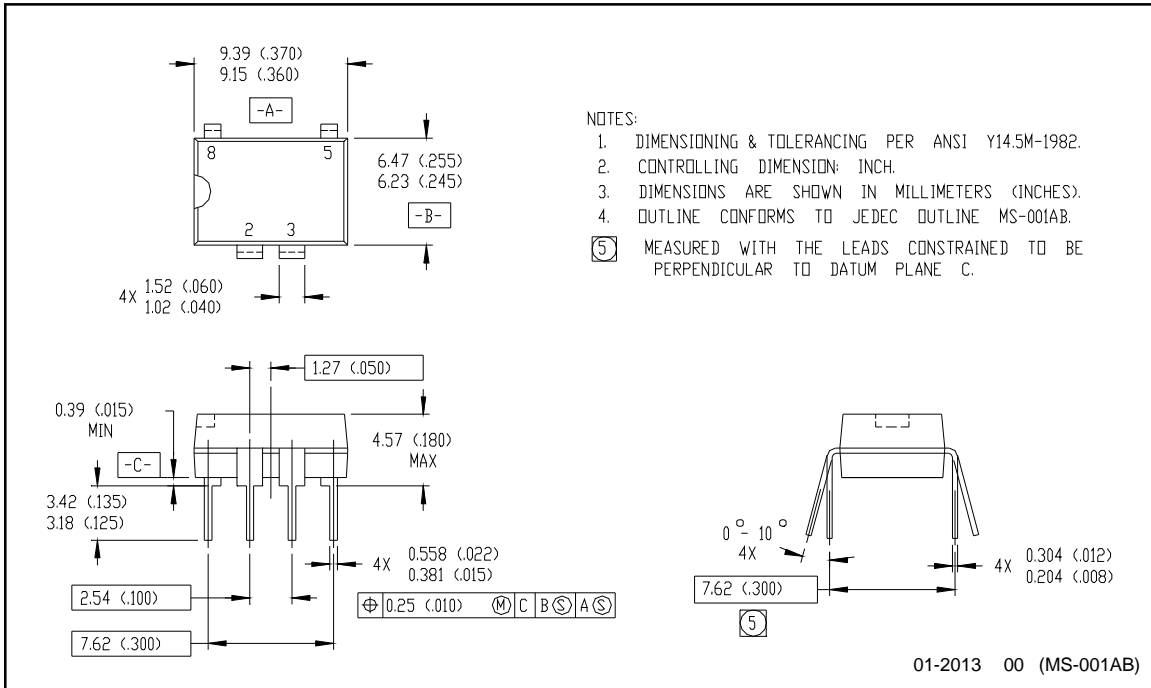


Figure 9. Typical Output Capacitance

Case Outline



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Data and specifications subject to change without notice. 11/1/2007