IOR HiRel
An Infineon Technologies Company

IRHNM57110
JANSR2N7503U8

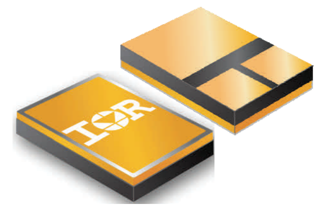
RADIATION HARDENED POWER MOSFET SURFACE MOUNT (SMD-0.2)

100V, N-CHANNEL
REF: MIL-PRF-19500/743
R5 TECHNOLOGY

Product Summary

Part Number	Radiation Level	RDS(on)	I_D	QPL Part Number
IRHNM57110	100 kRads(Si)	0.22Ω	6.9A	JANSR2N7503U8
IRHNM53110	300 kRads(Si)	0.22Ω	6.9A	JANSF2N7503U8
IRHNM55110	500 kRads(Si)	0.22Ω	6.9A	JANSG2N7503U8
IRHNM58110	1000 kRads(Si)	0.22Ω	6.9A	JANSH2N7503U8

Refer to page 9 for additional part number- IRHNMC57110 (Ceramic Lid)



SMD-0.2
(METAL LID)

Description

IR HiRel R5 technology provides high performance power MOSFETs for space applications. These devices have been characterized for Single Event Effects (SEE) with useful performance up to an LET of 80 (MeV/(mg/cm^2)). The combination of low Rdson and low gate charge reduces the power losses in switching applications such as DC to DC converters and motor control. These devices retain all of the well established advantages of MOSFETs such as voltage control, fast switching and temperature

Features

- Single Event Effect (SEE) Hardened
- Low RDS(on)
- Low Total Gate Charge
- Simple Drive Requirements
- Hermetically Sealed
- Surface Mount
- Ceramic Package
- Light Weight
- Complimentary P-Channel Available - IRHNM597110, IRHNMC597110

Absolute Maximum Ratings

Pre-Irradiation

Symbol	Parameter	Value	Units
I_{D1} @ V_{GS} = 12V, T_C = 25°C	Continuous Drain Current	6.9	A
I_{D2} @ V_{GS} = 12V, T_C = 100°C	Continuous Drain Current	4.4	
I_{DM} @ T_C = 25°C	Pulsed Drain Current ①	27.6	
P_D @ T_C = 25°C	Maximum Power Dissipation	23	W
	Linear Derating Factor	0.18	W/°C
V_{GS}	Gate-to-Source Voltage	± 20	V
E_{AS}	Single Pulse Avalanche Energy ②	24	mJ
I_{AR}	Avalanche Current ①	6.9	A
E_{AR}	Repetitive Avalanche Energy ①	2.3	mJ
dv/dt	Peak Diode Recovery dv/dt ③	11.5	V/ns
T_J T_{STG}	Operating Junction and Storage Temperature Range	-55 to + 150	°C
	Lead Temperature	300 (for 5s)	
	Weight	0.25 (Typical)	g

For footnotes refer to the page 2.

Pre-Irradiation

Electrical Characteristics @ Tj = 25°C (Unless Otherwise Specified)

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
BV_{DSS}	Drain-to-Source Breakdown Voltage	100	—	—	V	V_{GS} = 0V, I_D = 1.0mA
$\Delta BV_{DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	0.13	—	V/°C	Reference to 25°C, I_D = 1.0mA
$R_{DS(on)}$	Static Drain-to-Source On-Resistance	—	—	0.22	Ω	V_{GS} = 12V, I_{D2} = 4.4A ④
$V_{GS(th)}$	Gate Threshold Voltage	2.0	—	4.0	V	V_{DS} = V_{GS}, I_D = 1.0mA
$\Delta V_{GS(th)}/\Delta T_J$	Gate Threshold Voltage Coefficient	—	-7.5	—	mV/°C	
Gfs	Forward Transconductance	3.6	—	—	S	V_{DS} = 15V, I_{D2} = 4.4A ④
I_{DSS}	Zero Gate Voltage Drain Current	—	—	10	µA	V_{DS} = 80V, V_{GS} = 0V
		—	—	25		V_{DS} = 80V,V_{GS} = 0V,T_J =125°C
I_{GSS}	Gate-to-Source Leakage Forward	—	—	100	nA	V_{GS} = 20V
	Gate-to-Source Leakage Reverse	—	—	-100		V_{GS} = -20V
Q_G	Total Gate Charge	—	—	15	nC	I_{D1} = 6.9A
Q_{GS}	Gate-to-Source Charge	—	—	4.0		V_{DS} = 50V
Q_{GD}	Gate-to-Drain ('Miller') Charge	—	—	5.0		V_{GS} = 12V
$t_{d(on)}$	Turn-On Delay Time	—	—	6.6	ns	V_{DD} = 50V
Tr	Rise Time	—	—	8.0		I_{D1} = 6.9A
$t_{d(off)}$	Turn-Off Delay Time	—	—	34		R_G = 7.5Ω
Tf	Fall Time	—	—	15		V_{GS} = 12V
Ls +L_D	Total Inductance	—	6.8	—	nH	Measured from Drain lead (6mm / 0.25 in from package) to Source lead (6mm / 0.25 in from package) with Source wire internally bonded from Source pin to Drain pad
C_{iss}	Input Capacitance	—	378	—	pF	V_{GS} = 0V
C_{oss}	Output Capacitance	—	108	—		V_{DS} = 25V
C_{rss}	Reverse Transfer Capacitance	—	2.3	—		ƒ = 100KHz
R_G	Gate Resistance	—	8.0	—	Ω	ƒ = 1.0MHz, open drain

Source-Drain Diode Ratings and Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
I_S	Continuous Source Current (Body Diode)	—	—	6.9	A	
I_{SM}	Pulsed Source Current (Body Diode) ①	—	—	27.6		
V_{SD}	Diode Forward Voltage	—	—	1.2	V	T_J = 25°C,I_S = 6.9A, V_{GS} = 0V ④
t_{rr}	Reverse Recovery Time	—	—	144	ns	T_J = 25°C ,I_F = 6.9A, V_{DD} ≤ 50V
Q_{rr}	Reverse Recovery Charge	—	—	633	nC	di/dt = 100A/µs ④
t_{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by L_S+L_D)				

Thermal Resistance

Symbol	Parameter	Min.	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case	—	—	5.4	°C/W

Footnotes:

① Repetitive Rating; Pulse width limited by maximum junction temperature.

② V_{DD} = 50V, starting T_J = 25°C, L =1.0mH, Peak I_L = 6.9A, V_{GS} = 12V

③ I_{SD} ≤ 6.9A, di/dt ≤ 560A/µs, V_{DD} ≤ 100V, T_J ≤ 150°C

④ Pulse width ≤ 300 µs; Duty Cycle ≤ 2%

⑤ Total Dose Irradiation with V_{GS} Bias. 12 volt V_{GS} applied and V_{DS} = 0 during irradiation per MIL-STD-750, Method 1019, condition A.

⑥ Total Dose Irradiation with V_{DS} Bias. 80 volt V_{DS} applied and V_{GS} = 0 during irradiation per MIL-STD-750, Method 1019, condition A.

Radiation Characteristics

IR HiRel Radiation Hardened MOSFETs are tested to verify their radiation hardness capability. The hardness assurance program at International Rectifier is comprised of two radiation environments. Every manufacturing lot is tested for total ionizing dose (per notes 5 and 6) using the TO-3 package. Both pre- and post-irradiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison.

Table1. Electrical Characteristics @ Tj = 25°C, Post Total Dose Irradiation ⑤⑥

Symbol	Parameter	Up to 500 kRads (Si)[1]		1000 kRads (Si)[2]		Units	Test Conditions
		Min.	Max.	Min.	Max.		
BV_{DSS}	Drain-to-Source Breakdown Voltage	100	——	100	——	V	V_{GS} = 0V, I_D = 1.0mA
$V_{GS(th)}$	Gate Threshold Voltage	2.0	4.0	1.5	4.0	V	V_{DS} = V_{GS}, I_D = 1.0mA
I_{GSS}	Gate-to-Source Leakage Forward	——	100	——	100	nA	V_{GS} = 20V
I_{GSS}	Gate-to-Source Leakage Reverse	——	-100	——	-100	nA	V_{GS} = -20V
I_{DSS}	Zero Gate Voltage Drain Current	——	10	——	10	µA	V_{DS} = 48V, V_{GS} = 0V
$R_{DS(on)}$	Static Drain-to-Source ④ On-State Resistance (TO - 3)	——	0.226	——	0.246	Ω	V_{GS} = 12V, I_{D2} = 4.4A
$R_{DS(on)}$	Static Drain-to-Source ④ On-State Resistance (SMD – 0.2)	——	0.226	——	0.246	Ω	V_{GS} = 12V, I_{D2} = 4.4A
V_{SD}	Diode Forward Voltage ④	——	1.2	——	1.2	V	V_{GS} = 0V, I_S = 6.9A

1. Part numbers IRHNM57110 (JANSR2N7503U8), IRHNM53110 (JANSF2N7503U8), and IRHNM55110 (JANSG2N7503U8),
2. Part numbers IRHNM58110 (JANSH2N7503U8)

IR HiRel radiation hardened MOSFETs have been characterized in heavy ion environment for Single Event Effects (SEE). Single Event Effects characterization is illustrated in Fig. a and Table 2.

Table 2. Typical Single Event Effect Safe Operating Area

LET (MeV/(mg/cm²))	Energy (MeV)	Range (µm)	VDS (V)				
			@ VGS = 0V	@ VGS = -5V	@ VGS = -10V	@ VGS = -15V	@ VGS = -20V
38 ± 5%	300 ± 7.5%	38 ± 7.5%	100	100	100	100	100
61 ± 5%	330 ± 7.5%	31 ± 10%	100	100	100	35	25
84 ± 5%	350 ± 10%	28 ± 7.5%	100	100	80	25	——

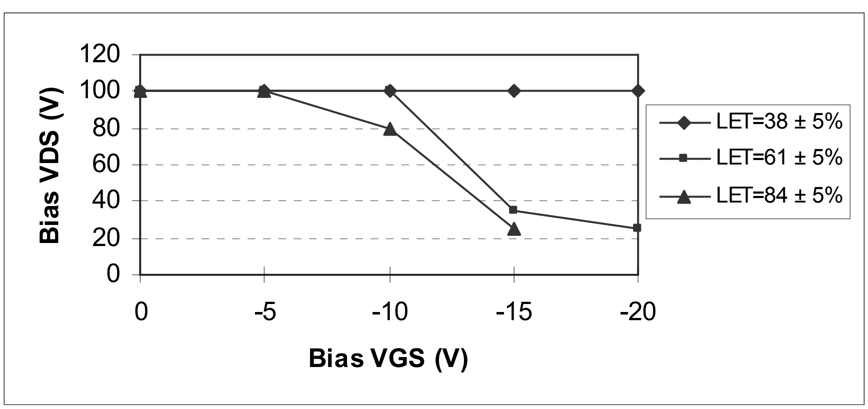


Fig a. Typical Single Event Effect, Safe Operating Area

For footnotes refer to the page 2.

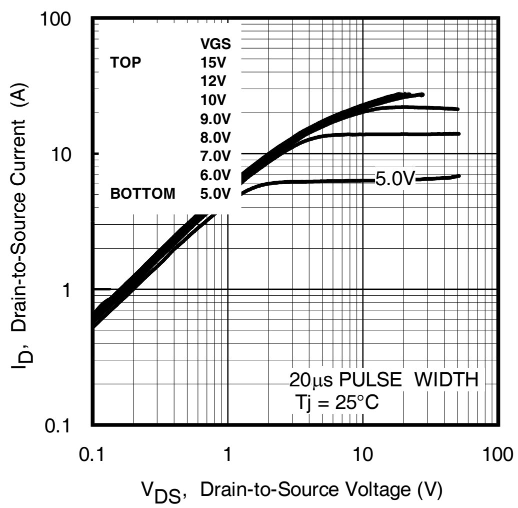


Fig 1. Typical Output Characteristics

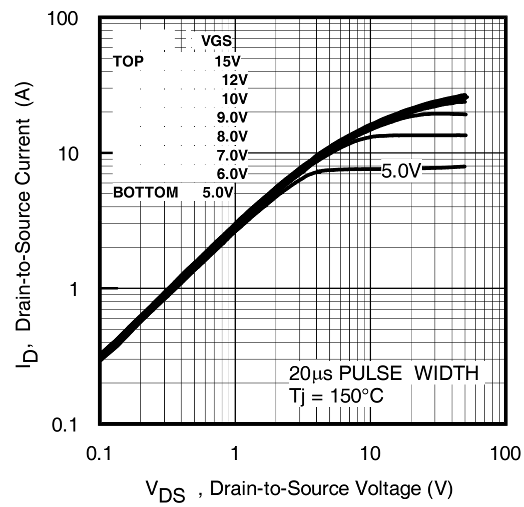


Fig 2. Typical Output Characteristics

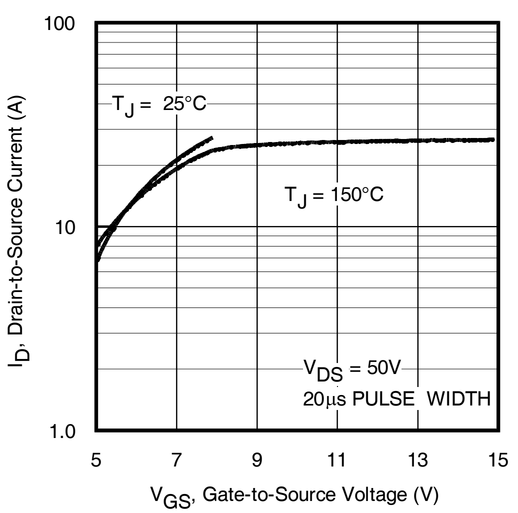


Fig 3. Typical Transfer Characteristics

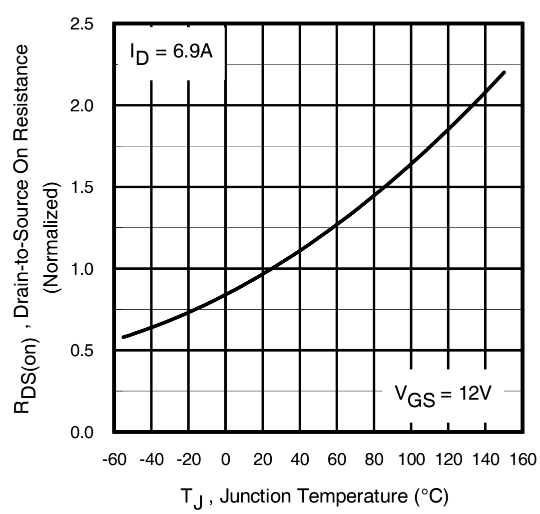


Fig 4. Normalized On-Resistance Vs. Temperature

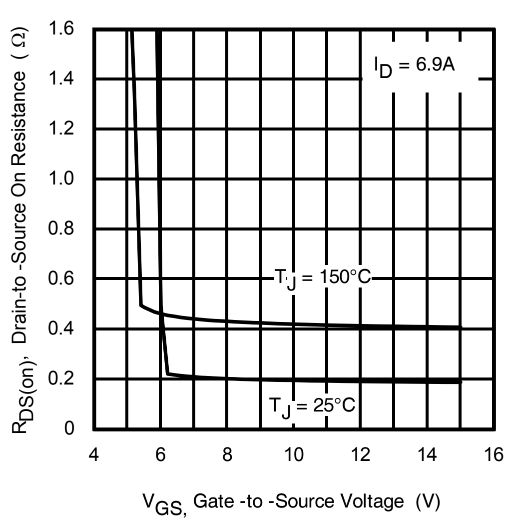


Fig 5. Typical On-Resistance Vs Gate Voltage

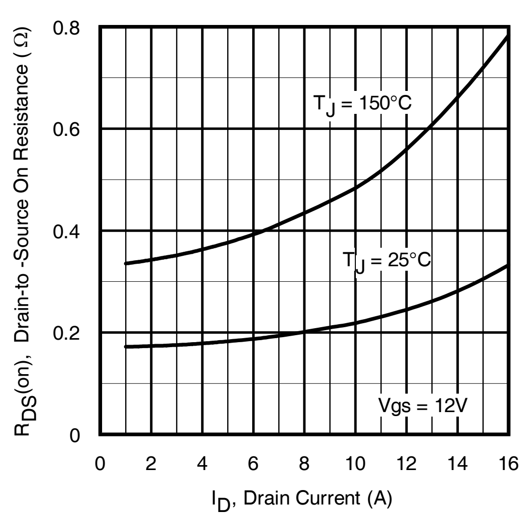


Fig 6. Typical On-Resistance Vs Drain Current

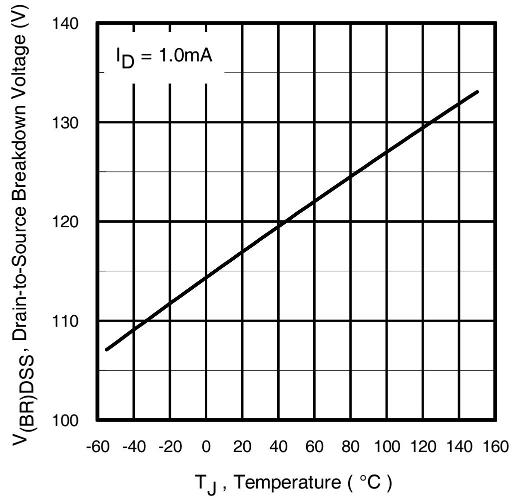


Fig 7. Typical Drain-to-Source Breakdown Voltage Vs Temperature

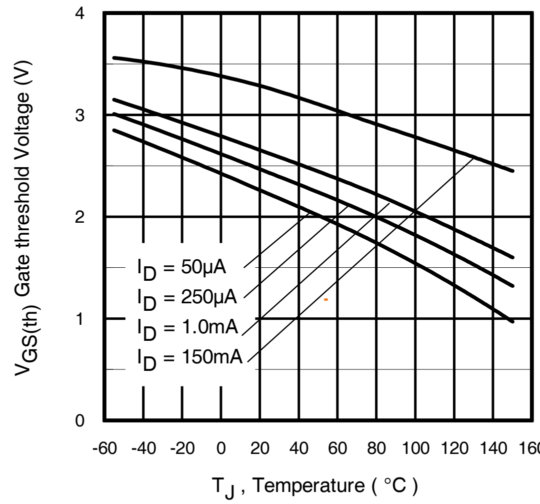


Fig 8. Typical Threshold Voltage Vs Temperature

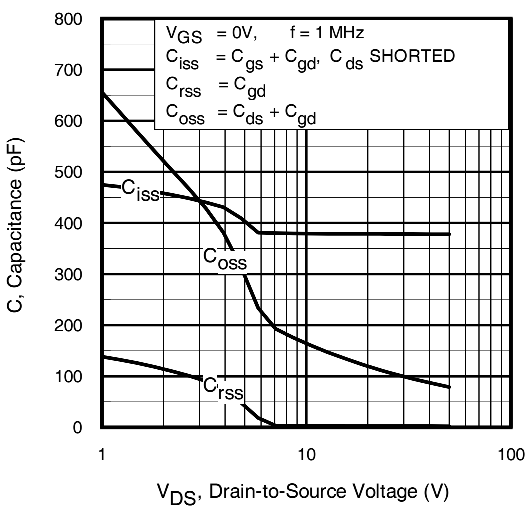


Fig 9. Typical Capacitance Vs. Drain-to-Source Voltage

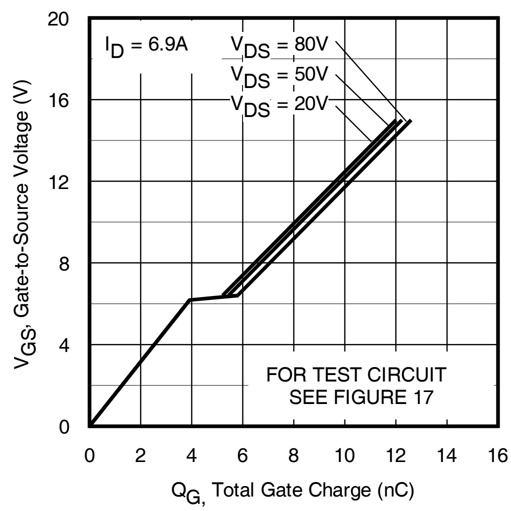


Fig 10. Typical Gate Charge Vs. Gate-to-Source Voltage

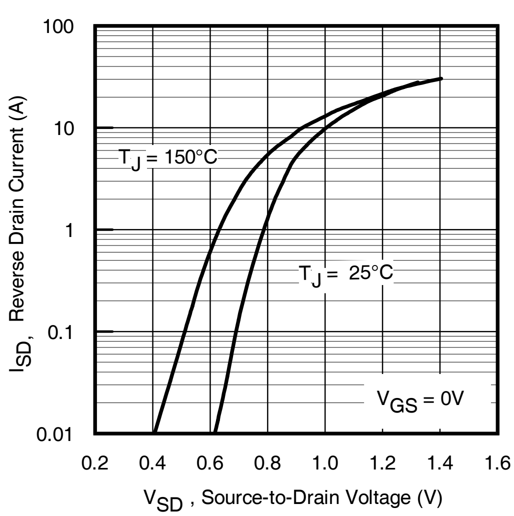


Fig 11. Typical Source-Drain Diode Forward Voltage

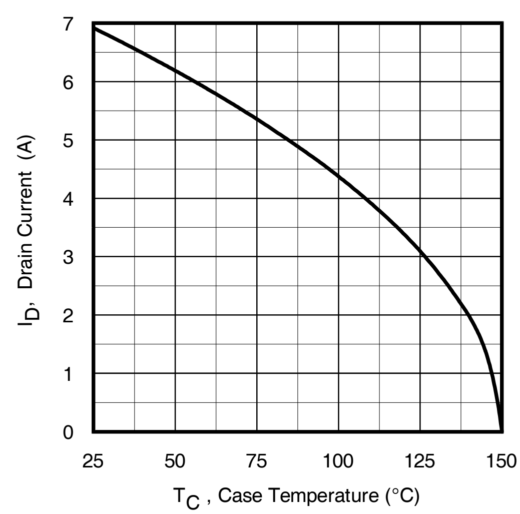


Fig 12. Maximum Drain Current Vs.Case Temperature

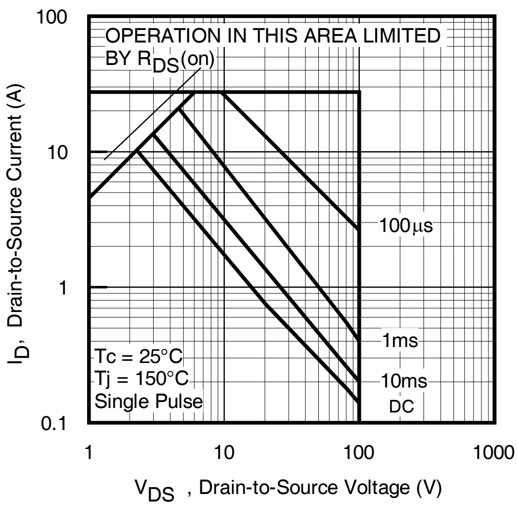


Fig 13. Maximum Safe Operating Area

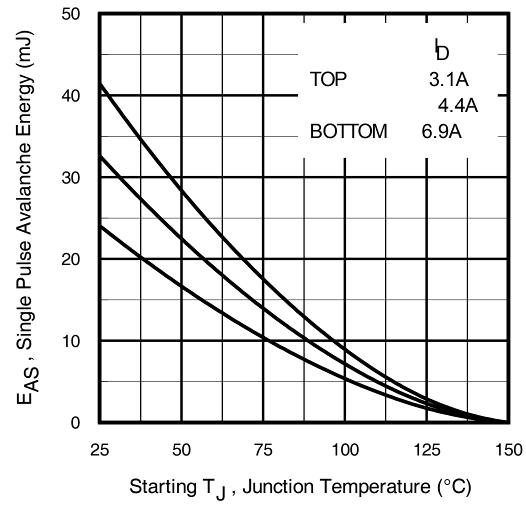


Fig 14. Maximum Avalanche Energy Vs. Drain Current

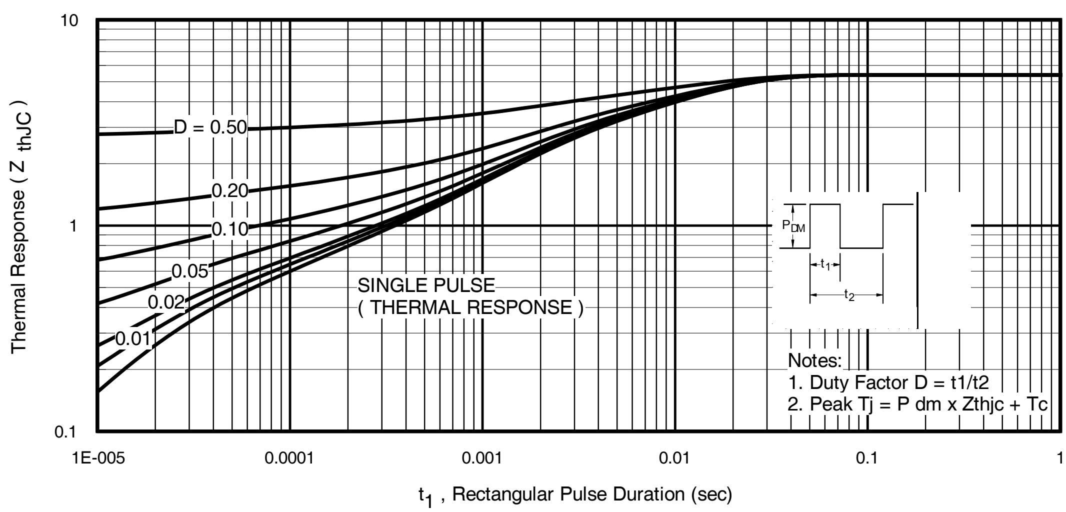


Fig 15. Maximum Effective Transient Thermal Impedance, Junction-to-Case

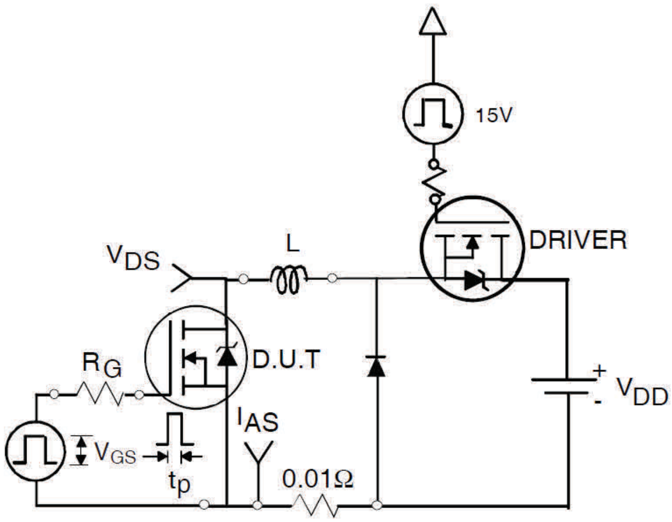


Fig 16a. Unclamped Inductive Test Circuit

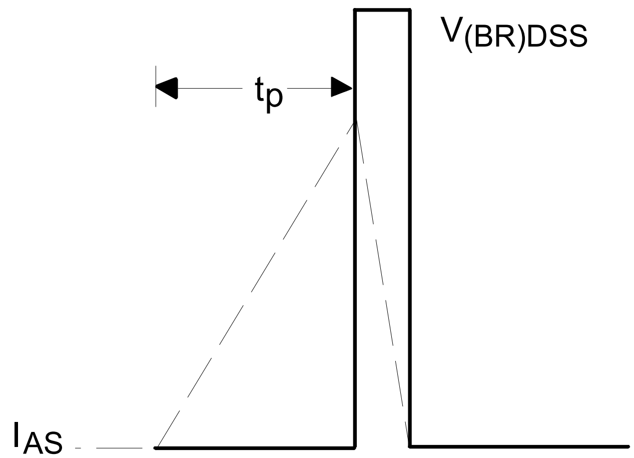


Fig 16b. Unclamped Inductive Waveforms

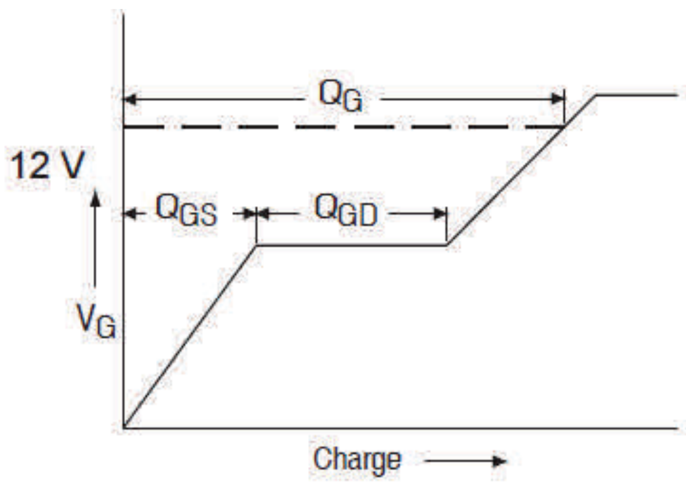


Fig 17a. Gate Charge Waveform

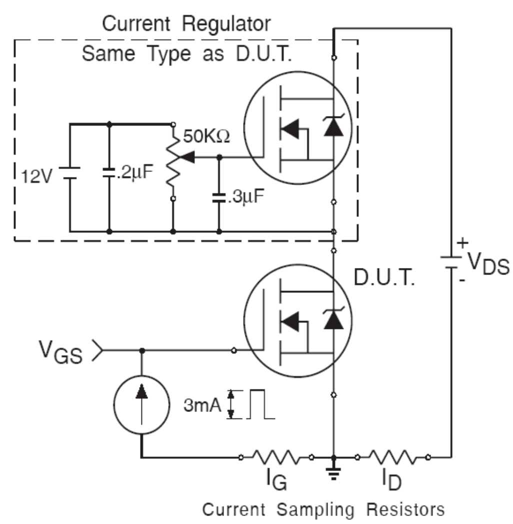


Fig 17b. Gate Charge Test Circuit

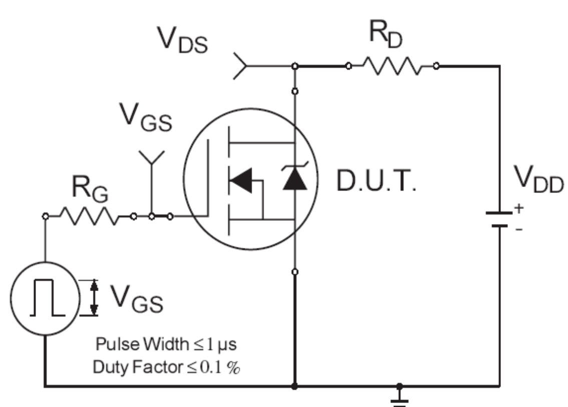


Fig 18a. Switching Time Test Circuit

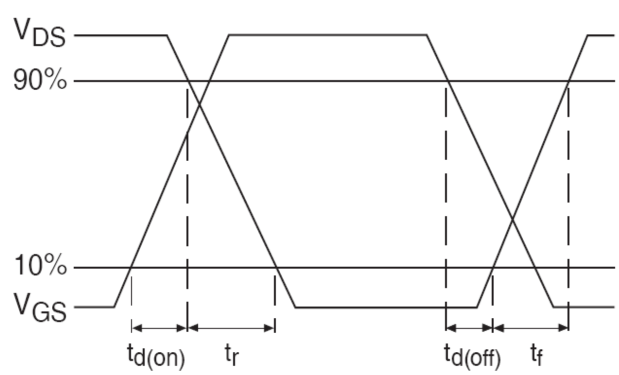
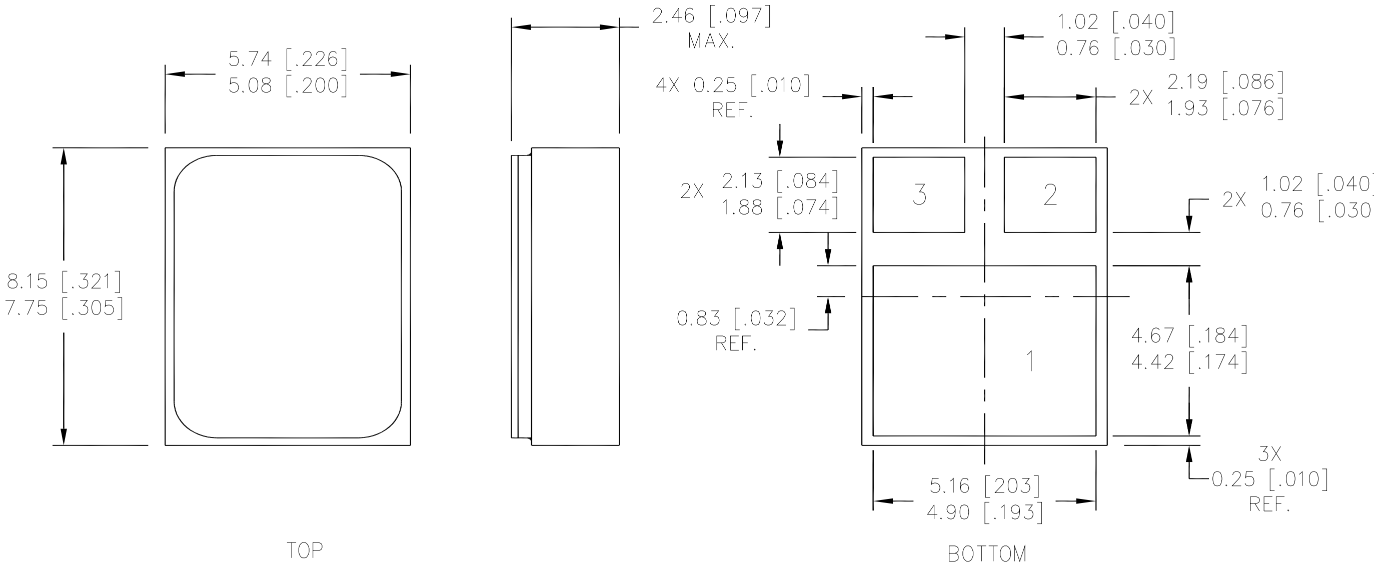


Fig 18b. Switching Time Waveforms

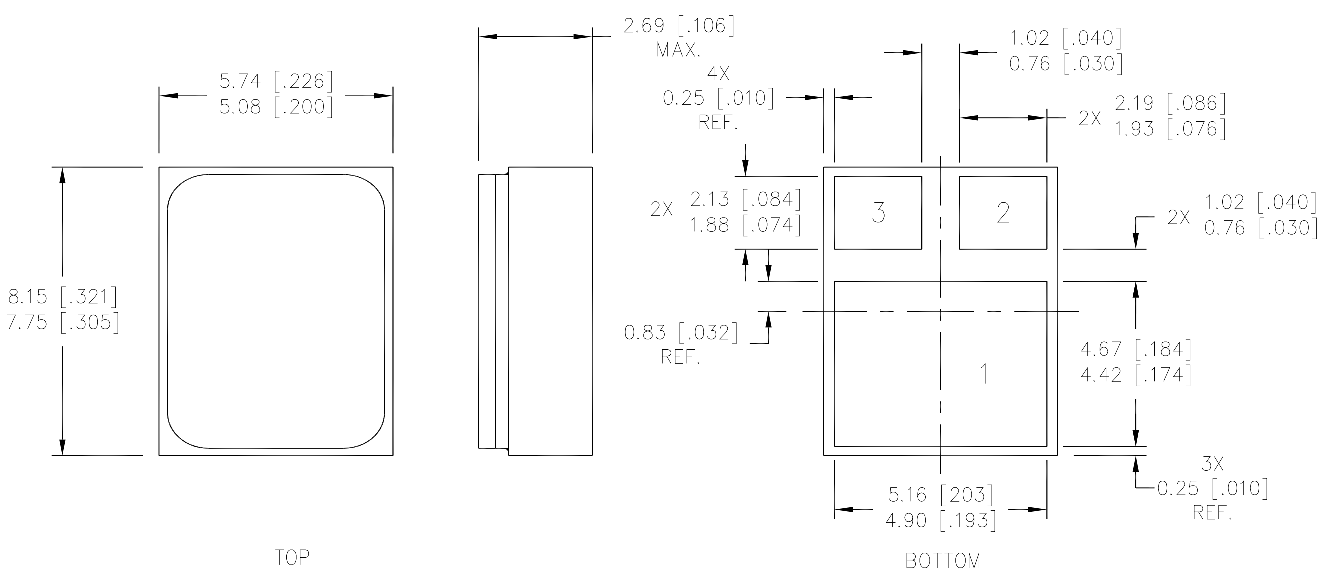
Case Outline and Dimensions - SMD-0.2 (Metal Lid)



NOTES:
1. DIMENSIONING & TOLERANCING PER ASME Y14.5M–1994.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSIONS ARE SHOWN IN INCHES [MILLIMETERS].

PAD ASSIGMENTS
1 = DRAIN
2 = GATE
3 = SOURCE

Case Outline and Dimensions - SMD-0.2 (Ceramic Lid)

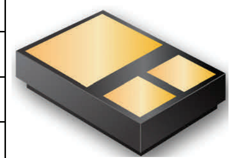


NOTES:
1. DIMENSIONING & TOLERANCING PER ASME Y14.5M–1994.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].

PAD ASSIGMENTS
1 = DRAIN
2 = GATE
3 = SOURCE

Additional Product Summary (continued from pages 1 and 3)

Product Summary

Part Number	Radiation Level	$R_{DS(on)}$	I_D	QPL Part Number	
IRHNMC57110	100 kRads(Si)	0.22Ω	6.9A	JANSR2N7503U8C	 SMD-0.2 (CERAMIC LID)
IRHNMC53110	300 kRads(Si)	0.22Ω	6.9A	JANSF2N7503U8C	
IRHNMC55110	500 kRads(Si)	0.22Ω	6.9A	JANSG2N7503U8C	
IRHNMC58110	1000 kRads(Si)	0.22Ω	6.9A	JANSH2N7503U8C	

An Infineon Technologies Company
www.infineon.com/irhirel

Infineon Technologies Service Center: USA Tel: +1 (866) 951-9519 and International Tel: +49 89 234 65555
Leominster, Massachusetts 01453, USA Tel: +1 (978) 534-5776
San Jose, California 95134, USA Tel: +1 (408) 434-5000
Data and specifications subject to change without notice.

IMPORTANT NOTICE

The information given in this document shall be in no event regarded as guarantee of conditions or characteristic. The data contained herein is a characterization of the component based on internal standards and is intended to demonstrate and provide guidance for typical part performance. It will require further evaluation, qualification and analysis to determine suitability in the application environment to confirm compliance to your system requirements.

With respect to any example hints or any typical values stated herein and/or any information regarding the application of the product, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind including without limitation warranties on non- infringement of intellectual property rights and any third party.

In addition, any information given in this document is subject to customer's compliance with its obligations stated in this document and any applicable legal requirements, norms and standards concerning customer's product and any use of the product of Infineon Technologies in customer's applications.

The data contained in this document is exclusively intended for technically trained staff. It is the responsibility of any customer's technical departments to evaluate the suitability of the product for the intended applications and the completeness of the product information given in this document with respect to applications.

For further information on the product, technology, delivery terms and conditions and prices, please contact your local sales representative or go to (www.infineon.com/hirel).

WARNING

Due to technical requirements products may contain dangerous substances. For information on the types in question, please contact your nearest Infineon Technologies office.