

**SCHOTTKY RECTIFIER
HIGH EFFICIENCY SERIES**

75 Amp. 45V

Major Ratings and Characteristics

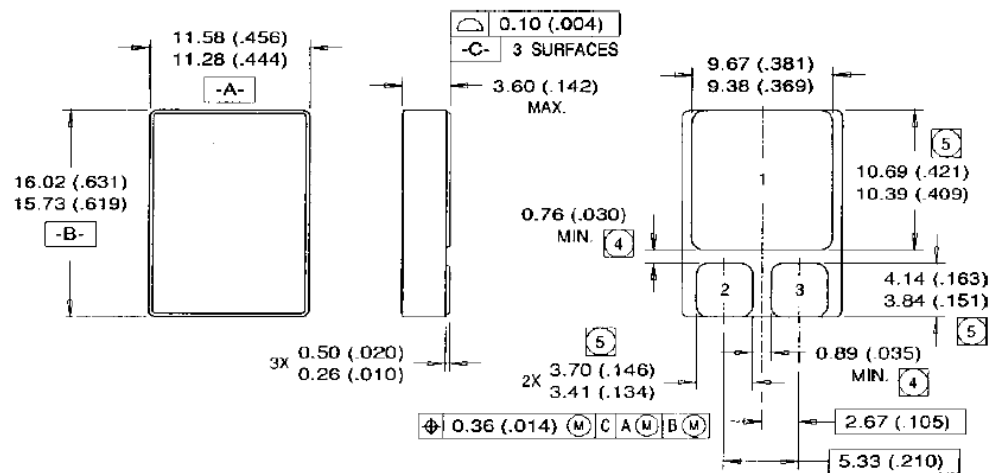
Characteristics	75SLQ045	Units
$I_{F(AV)}$	75	A
V_{RRM}	45	V
I_{FSM} @ $t_p = 8.3ms$ half-sine	500	A
V_F @ 75Apk, $T_J = 125^\circ C$	0.6	V
T_J, T_{stg} Operating and storage	-55 to 150	$^\circ C$

Description/Features

The 75SLQ045 Schottky rectifier has been expressly designed to meet the rigorous requirements of hi-rel environments. It is packaged in the hermetic surface mount SMD-1 ceramic package. The device's forward voltage drop and reverse leakage current are optimized for the lowest power loss and the highest circuit efficiency for typical high frequency switching power supplies and resonant power converters. Full MIL-PRF-19500 quality conformance testing is available on source control drawings to TX, TXV and S quality levels.

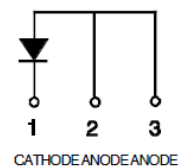
- Hermetically Sealed
- Low Forward Voltage Drop
- High Frequency Operation
- Guard Ring for Enhanced Ruggedness and Long term Reliability
- Surface Mount
- Lightweight

CASE STYLE



NOTES:

1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
4. DIMENSION INCLUDES METALLIZATION FLASH.
5. DIMENSION DOES NOT INCLUDE METALLIZATION FLASH.



Case Outline and Dimensions - SMD-1

Voltage Ratings

Part Number	75SLQ045
V_R Max. DC Reverse Voltage (V)	45
V_{RRM} Max. Working Peak Reverse Voltage (V)	

Absolute Maximum Ratings

Parameter	Limits	Units	Conditions
$I_{F(AV)}$ Max. Average Forward Current See Fig. 5	75	A	50% duty cycle @ $T_C = 78^\circ\text{C}$, square waveform
I_{FSM} Max. Peak One Cycle Non - Repetitive Surge Current	500	A	@ $t_p = 8.3$ ms half-sine

Electrical Specifications

Parameter	Limits	Units	Conditions	
V_{FM} Max. Forward Voltage Drop See Fig. 1①	0.68	V	@ $I_F = 75\text{A}$	$T_J = -55^\circ\text{C}$
	0.87	V	@ $I_F = 150\text{A}$	
	0.64	V	@ $I_F = 75\text{A}$	$T_J = 25^\circ\text{C}$
	0.90	V	@ $I_F = 150\text{A}$	
	0.61	V	@ $I_F = 75\text{A}$	$T_J = 125^\circ\text{C}$
	0.80	V	@ $I_F = 150\text{A}$	
I_{RM} Max. Reverse Leakage Current	1	mA	$T_J = 25^\circ\text{C}$	$V_R = \text{rated } V_R$
	77	mA	$T_J = 100^\circ\text{C}$	
	375	mA	$T_J = 125^\circ\text{C}$	
C_T Max. Junction Capacitance	2700	pF	$V_R = 5V_{DC}$ (1MHz, 25°C)	
L_S Typical Series Inductance	5.9	nH	Measured from center of cathode pad to center of anode pad	

Thermal-Mechanical Specifications

Parameter	Limits	Units	Conditions
T_J Max. Junction Temperature Range	-55 to 150	$^\circ\text{C}$	
T_{stg} Max. Storage Temperature Range	-55 to 150	$^\circ\text{C}$	
R_{thJC} Max. Thermal Resistance, Junction to Case	1.1	$^\circ\text{C/W}$	DC operation See Fig. 4
W_t Weight (Typical)	2.6	g	
Die Size (Typical)	200 x 200	mils	
Case Style	SMD-1		

- ① Pulse Width < 300 μs , Duty Cycle < 2%
- ② Pins 2 and 3 externally tied together

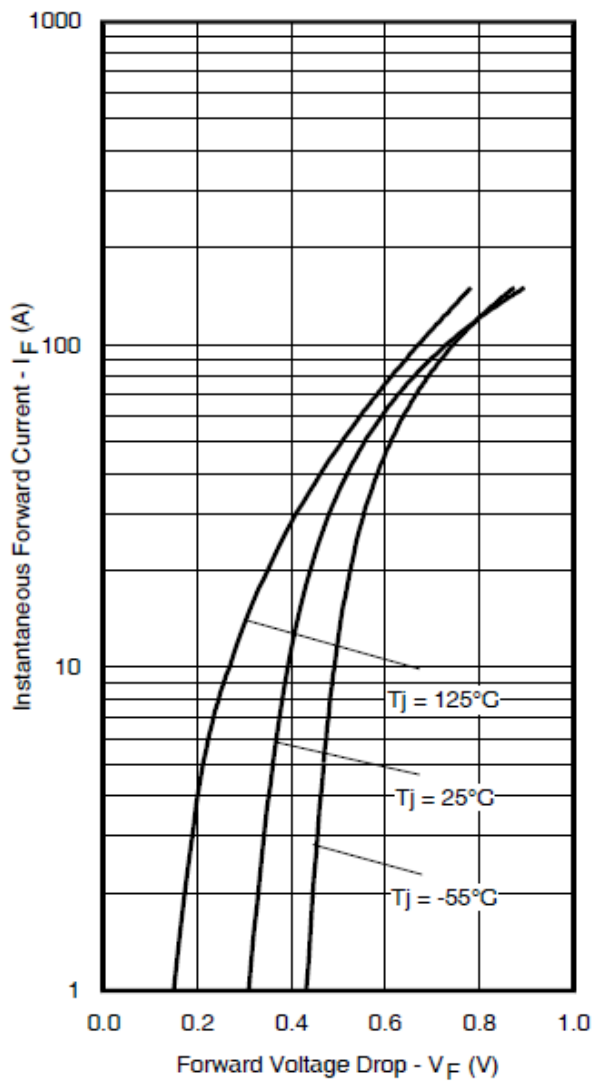


Fig 1. Max. Forward Voltage Drop Characteristics

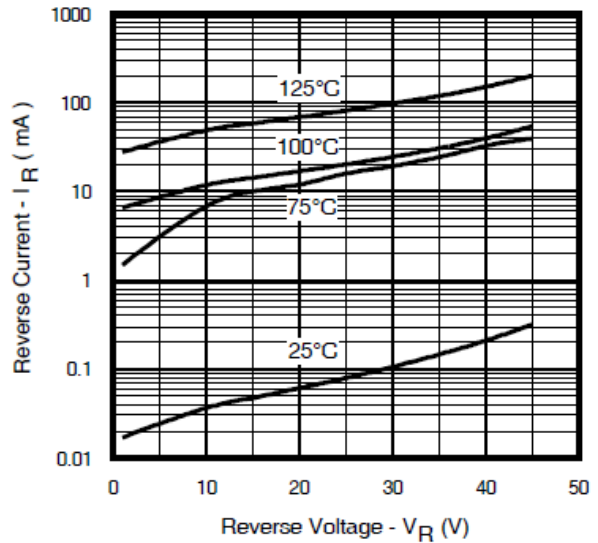


Fig 2. Typical Values of Reverse Current Vs. Reverse Voltage

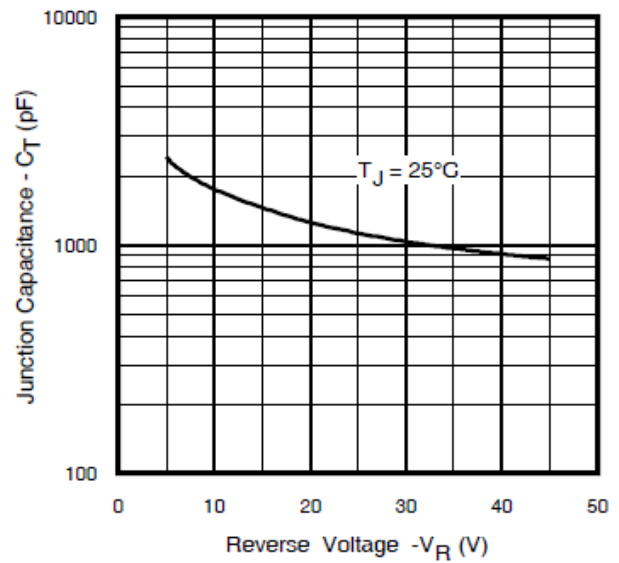


Fig 3. Typical Junction Capacitance Vs. Reverse Voltage

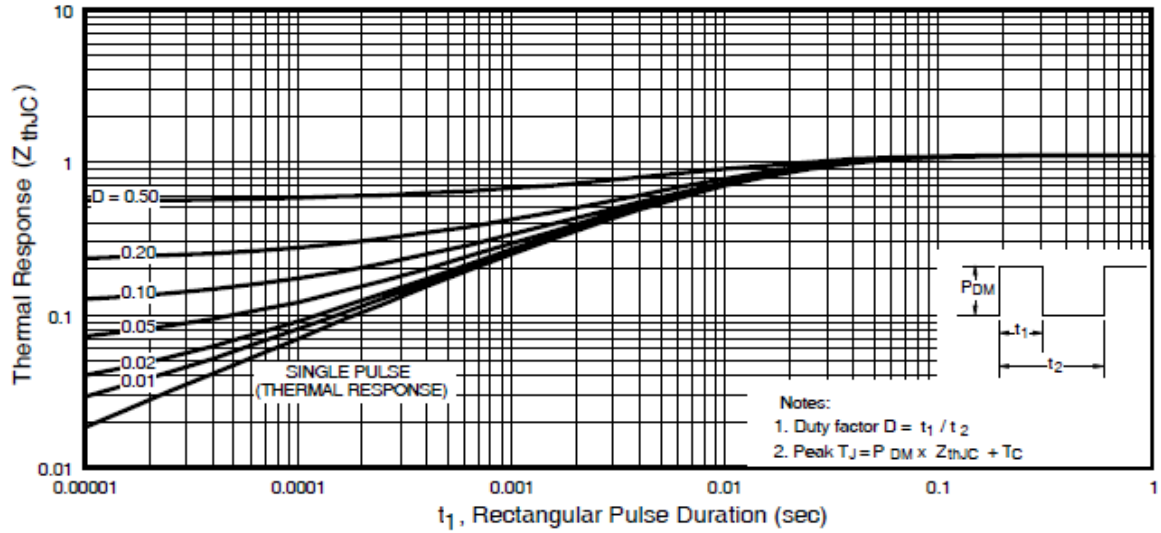


Fig 4. Max. Thermal Impedance Z_{thJC} Characteristics

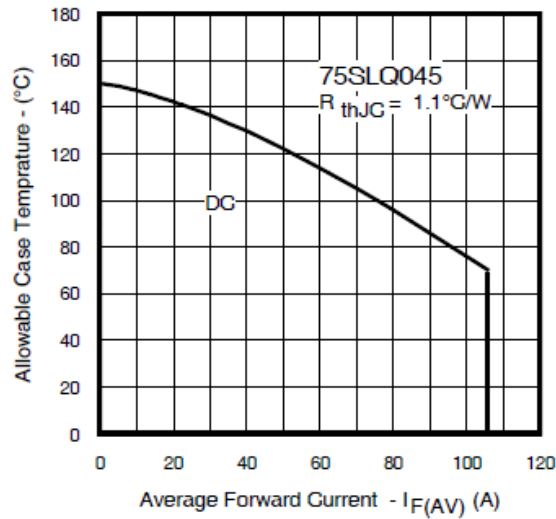


Fig 5. Max. Allowable Case Temperature Vs. Average Forward Current

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The information given in this document shall be in no event regarded as guarantee of conditions or characteristic. The data contained herein is a characterization of the component based on internal standards and is intended to demonstrate and provide guidance for typical part performance. It will require further evaluation, qualification and analysis to determine suitability in the application environment to confirm compliance to your system requirements.

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